



The Consultative Committee for Space Data Systems

**Recommendation for
Space Data System Standards**

**NON-COHERENT OPTICAL
COMMUNICATIONS
CODING AND
SYNCHRONIZATION**

RECOMMENDED STANDARD

CCSDS 142.0-B-2

BLUE BOOK
March 2026

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FOREWORD

This document is a CCSDS Recommended Standard for the channel coding and synchronization of signals to be used in free space optical communications systems of space missions. The channel coding and synchronization concepts described herein are intended for missions that are cross-supported between Agencies of the CCSDS.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights. CCSDS has processes for identifying patent issues and for securing from the patent holder agreement that all licensing policies are reasonable and non-discriminatory. However, CCSDS does not have a patent law staff, and CCSDS shall not be held responsible for identifying any or all such patent rights.

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CCSDS RECOMMENDED STANDARD FOR NON-COHERENT
OPTICAL COMMUNICATIONS CODING AND SYNCHRONIZATION

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- Swiss Space Office (SSO)/Switzerland.
- United States Geological Survey (USGS)/USA.

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NOTE – Sections 4 and 6 and annexes C and D are new in their entirety and are presented without markup to improve readability.

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1 INTRODUCTION

1.1 PURPOSE

The purpose of this Recommended Standard is to specify the channel coding and synchronization schemes for free space optical communications systems used by space missions.

The primary applications addressed in this issue of the Recommended Standard are space-to-ground and ground-to-space links through an atmospheric channel in a photon-starved regime and a high photon flux regime; use of the Recommended Standard for other applications or operating conditions is not precluded.

In photon-starved links, the photon-efficiency of the link is of primary concern. When provided with a set of CCSDS transfer frames produced by the Data Link Protocol Sublayer (as specified in reference [1], [2], or [8]), this specification allows one to determine the binary vector to be provided to the Physical Layer. The 'ones' and 'zeroes' of the binary vector indicate the slots that are to be pulsed and non-pulsed, respectively, in the optical transmission. The physical characteristics of such transmissions are addressed in *Optical Communications Physical Layer* (reference [3]).

In high photon flux links, the number of photons per second is in the order of 10^{10} and is not a concern, and a pulse position modulation is not required.

1.2 SCOPE

This Recommended Standard defines Coding and Synchronization Sublayer schemes in terms of the signal characteristics and procedures involved in the encoding and synchronization of the optical signals. It does not specify

- a) individual implementations or products;
- b) the methods or technologies required to perform the procedures; or
- c) the management activities required to configure and control the system.

This issue includes a specification for High Photon Efficiency (HPE) systems, in which the photon-efficiency of the link is of primary concern, and a specification for Optical On-Off Keying (OOK) systems, which is designed for high photon flux links.

1.3 APPLICABILITY

This Recommended Standard applies to the creation of agency standards and to the future data communications over optical space links between CCSDS Agencies in cross-support situations. It includes comprehensive specifications of the data formats and procedures for inter-agency cross support. It is neither a specification of nor a design for real systems that may be implemented for existing or future missions.

The Recommended Standard specified in this document is to be invoked through the normal standards program of each CCSDS Agency and is applicable to those missions for which cross support based on capabilities described in this Recommended Standard is anticipated. Where mandatory capabilities are clearly indicated in sections of this Recommended Standard, they must be implemented when this document is used as a basis for cross support. Where options are allowed or implied, implementation of these options is subject to specific bilateral cross-support agreements between the agencies involved.

1.4 RATIONALE

This Recommended Standard facilitates cross support at the Coding and Synchronization Sublayer of optical communications systems used by CCSDS Member Agencies. Such cross support requires specification of the slicing of transfer frames, the synchronization markers, channel encoding, modulation, channel interleaving, slot mapping, and guard slot insertion, among other details of the Coding and Synchronization Sublayer.

The CCSDS believes it is important to document the rationale underlying the recommendations chosen so that future evaluations of proposed changes or improvements will not lose sight of previous decisions. The rationale for the specifications making up this Recommended Standard is expected to be documented in a forthcoming CCSDS Informational Report.

1.5 DOCUMENT STRUCTURE

This document is divided into seven numbered sections and seven annexes:

- a) section 1 presents the purpose, scope, applicability, rationale, document structure, definitions, nomenclature, conventions, and references;
- b) section 2 provides an overview of the architecture and summary of functions of the optical Coding and Synchronization Sublayer;
- c) section 3 specifies telemetry signaling for HPE;
- d) section 4 specifies telemetry signaling for O3K;
- e) section 5 specifies HPE beacon and optional Advanced Orbiting Systems (AOS) transfer frame signaling;
- f) section 6 lists the managed parameters;
- g) annex A is the Protocol Implementation Conformance Statement (PICS) proforma;
- h) annex B defines the service provided to the users;
- i) annex C provides exponent matrix tables for LDPC Codes;
- j) annex D specifies the generation of Gold sequences;
- k) annex E discusses security, SANA, and patent considerations;

- l) annex F lists abbreviations and terms used within this document;
- m) annex G provides a list of informative references.

1.6 DEFINITIONS

1.6.1 DEFINITIONS FROM THE OPEN SYSTEM INTERCONNECTION BASIC REFERENCE MODEL

This Recommended Standard makes use of a number of terms defined in reference [4]. The use of those terms in this Recommended Standard is to be understood in a generic sense, that is, in the sense that those terms are generally applicable to any of a variety of technologies that provide for the exchange of information between real systems. Those terms are

- a) Data Link Layer;
- b) Physical Layer;
- c) service; and
- d) service data unit.

1.6.2 DEFINITIONS FROM OPEN SYSTEM INTERCONNECTION SERVICE DEFINITION CONVENTIONS

This Recommended Standard makes use of a number of terms defined in reference [5]. The use of those terms in this Recommended Standard is to be understood in a generic sense, that is, in the sense that those terms are generally applicable to any of a variety of technologies that provide for the exchange of information between real systems. Those terms are

- a) indication;
- b) primitive;
- c) request;
- d) service provider; and
- e) service user.

1.7 NOMENCLATURE

1.7.1 NORMATIVE TEXT

The following conventions apply for the normative specifications in this Recommended Standard:

- a) the words 'shall' and 'must' imply a binding and verifiable specification;
- b) the word 'should' implies an optional, but desirable, specification;

- c) the word ‘may’ implies an optional specification;
- d) the words ‘is’, ‘are’, and ‘will’ imply statements of fact.

1.7.2 INFORMATIVE TEXT

In the normative sections of this document, informative text is set off from the normative specifications either in notes or under one of the following subsection headings:

- Overview;
- Background;
- Rationale;
- Discussion.

1.8 CONVENTIONS

In this document, the following convention is used to identify each bit in an N -bit field. The first bit in the field to be transmitted (i.e., the most left justified when drawing a figure) is defined to be ‘Bit 0’, the following bit is defined to be ‘Bit 1’, and so on, up to ‘Bit $N-1$ ’. When the field is used to express a binary value (such as a counter), the Most Significant Bit (MSB) shall be the first transmitted bit of the field, that is, ‘Bit 0’ (see Figure 1-1: Bit Numbering Convention).

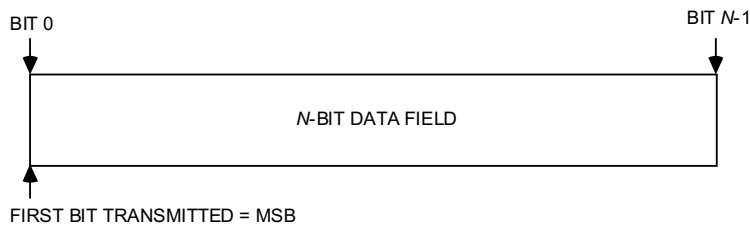


Figure 1-1: Bit Numbering Convention

In accordance with standard data-communications practice, data fields are often grouped into 8-bit ‘words’ that conform to the above convention. Throughout this specification, such an 8-bit word is called an ‘octet’. The numbering for octets within a data structure starts with ‘0’.

NOTE – Throughout this document, ‘bit’ refers to the contents of the transfer frames. A bit is a binary digit transferred between the Data Link Protocol Sublayer and the Coding and Synchronization Sublayer. Other symbols, whether binary or nonbinary, are referred to by other names, such as ‘binary digits’. It should be understood that the ordering conventions described above apply equally to other types of symbols.

1.9 REFERENCES

The following publications contain provisions which, through reference in this text, constitute provisions of this Recommended Standard. At the time of publication, the editions indicated were valid. All documents are subject to revision, and users of this Recommended Standard are encouraged to investigate the possibility of applying the most recent editions of the documents indicated below. The CCSDS Secretariat maintains a register of currently valid CCSDS documents.

- [1] *TM Space Data Link Protocol*. Issue 3. Recommendation for Space Data System Standards (Blue Book), CCSDS 132.0-B-3. Washington, D.C.: CCSDS, October 2021.
- [2] *AOS Space Data Link Protocol*. Issue 4. Recommendation for Space Data System Standards (Blue Book), CCSDS 732.0-B-4. Washington, D.C.: CCSDS, October 2021.
- [3] *Non-Coherent Optical Communications Physical Layer*. Issue 2. Recommendation for Space Data System Standards (Blue Book), CCSDS 141.0-B-2. March 2026.
- [4] *Information Technology—Open Systems Interconnection—Basic Reference Model: The Basic Model*. 2nd ed. International Standard, ISO/IEC 7498-1:1994. Geneva: ISO, 1994.
- [5] *Information Technology—Open Systems Interconnection—Basic Reference Model—Conventions for the Definition of OSI Services*. International Standard, ISO/IEC 10731:1994. Geneva: ISO, 1994.
- [6] *TC Synchronization and Channel Coding*. Issue 4. Recommendation for Space Data System Standards (Blue Book), CCSDS 231.0-B-4. Washington, D.C.: CCSDS, July 2021.
- [7] *TM Synchronization and Channel Coding*. Issue 4. Recommendation for Space Data System Standards (Blue Book), CCSDS 131.0-B-4. Washington, D.C.: CCSDS, April 2022.
- [8] *Unified Space Data Link Protocol*. Issue 2. Recommendation for Space Data System Standards (Blue Book), CCSDS 732.1-B-2. Washington, D.C.: CCSDS, October 2021.

2 OVERVIEW

2.1 ARCHITECTURE

Figure 2-1: Relationship with OSI Layers illustrates the relationship of this Recommended Standard to the Open System Interconnection (OSI) reference model (reference [4]). Two sublayers of the Data Link Layer are defined for CCSDS space link protocols. The Data Link Protocol Sublayer provides functions for producing transfer frames; possible Space Data Link Protocols using optical communications are the Telemetry (TM) Space Data Link Protocol (reference [1]), the AOS Space Data Link Protocol (reference [2]), and the Unified Space Data Link Protocol (USLP) (reference [8]). The Optical Communications Coding and Synchronization Protocol specified in this Recommended Standard provides the functions of the Synchronization and Channel Coding Sublayer of the Data Link Layer for transferring transfer frames over an optical space link.

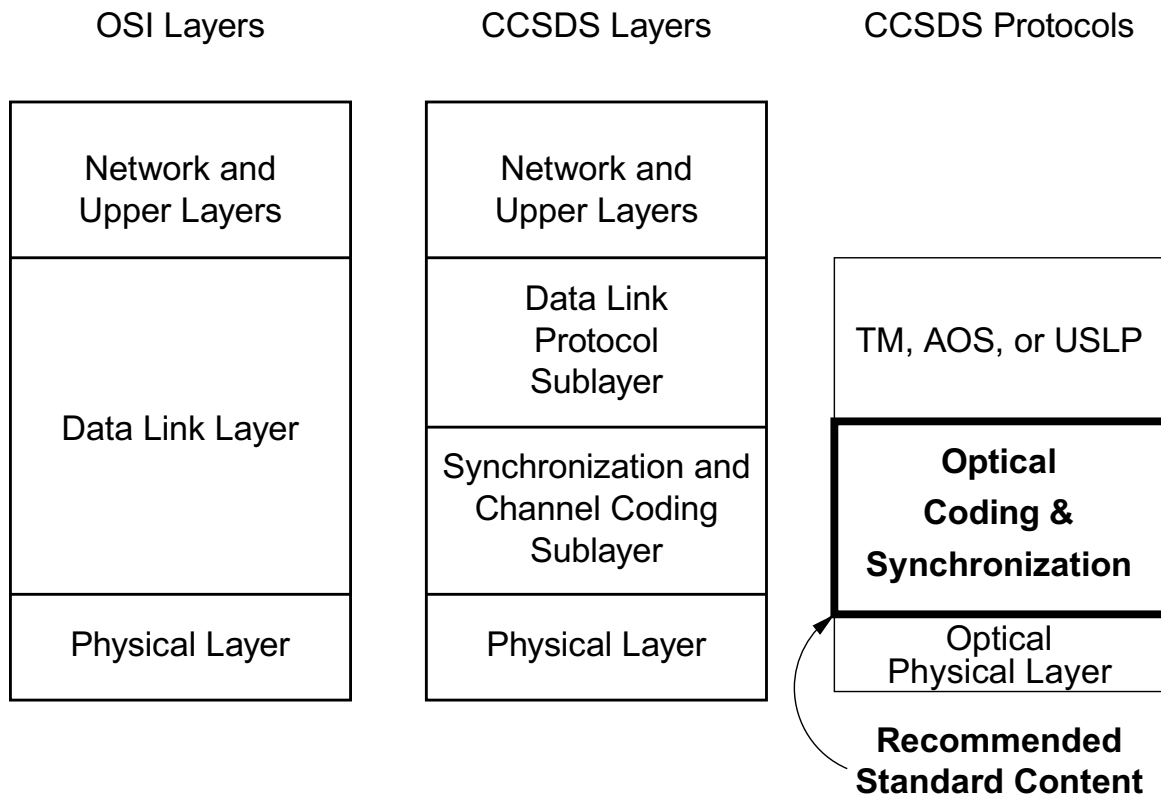


Figure 2-1: Relationship with OSI Layers

2.2 SUMMARY OF FUNCTIONS

The Non-Coherent Optical Coding and Synchronization Sublayer provides the following functions for transferring transfer frames over an optical space link:

- a) channel coding;
- b) synchronization; and
- c) telemetry transfer frame validation.

This Recommended Standard includes a specification for the transmission of telemetry transfer frames, AOS transfer frames, or fixed-length USLP transfer frames, and a separate specification for beacon and optional transmission of AOS transfer frames or fixed-length USLP transfer frames. In a typical implementation, telemetry signaling would occur from space to ground (downlink), and beacon and optional AOS/USLP transfer frame signaling would occur from ground to space (uplink), but this Recommended Standard does not prescribe the link direction or geometry. These transmissions occur simultaneously and continuously at opposite ends of the link during each communications session.

The telemetry link specification defines the relationship between input CCSDS transfer frames and output signal. This includes the following functions: CCSDS transfer frame adaptation, Cyclic Redundancy Check (CRC), channel coding including frame validation, modulation, channel interleaving, codeword synchronization marking, repetition, pseudo-randomization, sync-layer framing including sync layer signaling, slot mapping, and guard slot insertion. Some functions only apply to the HPE regime, and others only to the O3K regime.

The beacon specification, applicable only to the HPE regime, includes optional transmission of AOS/USLP transfer frames. The specification defines the relationship between sending-end input frames (from upper layer) and output pulsed slots (to lower layer). The specification includes functions to: 1) provide a reference beacon, 2) aid synchronization, and 3) support an AOS/USLP transfer frame transmission capability.

The overall architecture of the optical communications system is shown in figure Figure 2-2. Throughout the communications session, the optical Terminal A may optionally transmit a beacon, in which case the Terminal B receiver locks onto the beacon and uses it to assist in accurately pointing its optical transmitter. In the case of HPE, the beacon may be accompanied by optional AOS/USLP transfer frame data, which is decoded onboard. O3K allows no data to accompany the beacon. Telemetry is transmitted from Terminal B and received by Terminal A.

This Recommended Standard specifies the coding and synchronization features of the Terminal A and Terminal B transmitters, and a few details of the functions required at the receivers, including frame validation. In a typical application, Terminal A on the ground transmits an uplink beacon and optional AOS/USLP transfer frame data to Terminal B in space, and Terminal B transmits a downlink telemetry signal to Terminal A.

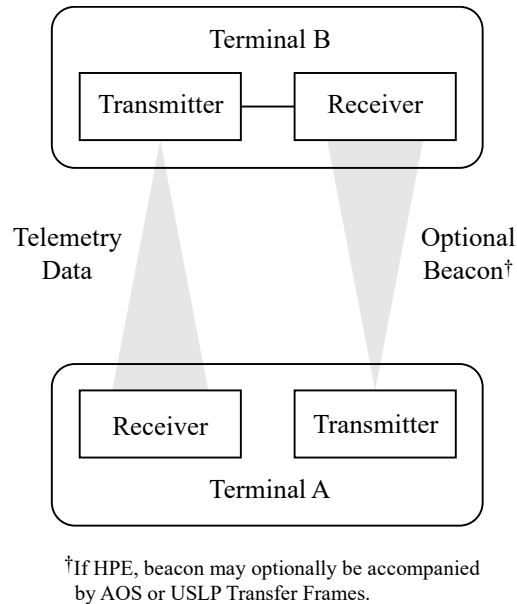


Figure 2-2: Overall Architecture of the Optical Communications System

2.3 INTERNAL ORGANIZATION OF TELEMETRY SIGNALING AT THE CODING AND SYNCHRONIZATION SUBLAYER

2.3.1 TELEMETRY SIGNALING AT THE SENDING END

Figure 2-3 shows the internal organization of the Coding and Synchronization Sublayer of telemetry signaling at the sending end. This figure identifies functions performed by the sublayer and shows logical relationships among these functions. The figure is not intended to imply any hardware or software configuration in a real system.

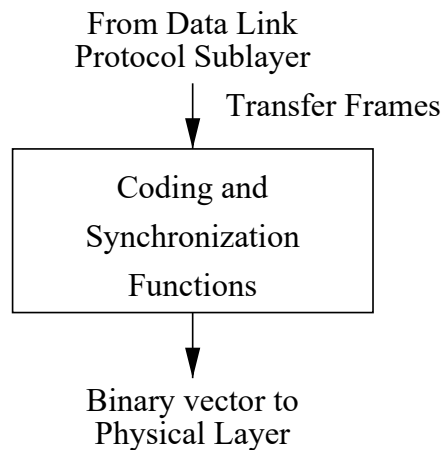


Figure 2-3: Internal Organization of Telemetry Signaling at the Sending End

At the sending end, the Coding and Synchronization Sublayer accepts transfer frames of fixed length and constant rate from the Data Link Protocol Sublayer (see Figure 2-1), performs functions selected for the mission, and delivers a binary vector to the Physical Layer. For HPE, which uses Pulse-Position Modulation (PPM), the binary vector indicates which slots are to contain light pulses. For O3K, the binary vector indicates which On-Off Keying (OOK) symbols are to contain light pulses.

2.3.2 TELEMETRY SIGNALING AT THE RECEIVING END

2.3.2.1 General

Figure 2-4 shows the internal organization of the Coding and Synchronization Sublayer for telemetry signaling at the receiving end. This figure identifies functions performed by the sublayer and shows logical relationships among these functions. The figure is not intended to imply any hardware or software configuration in a real system. Organization of CCSDS optical communications sublayering differs from that of the CCSDS protocol specifications for Radio Frequency (RF) communications in that the demodulation function is specified at the Coding and Synchronization Sublayer (this document) rather than at the Physical Layer (reference [3]).

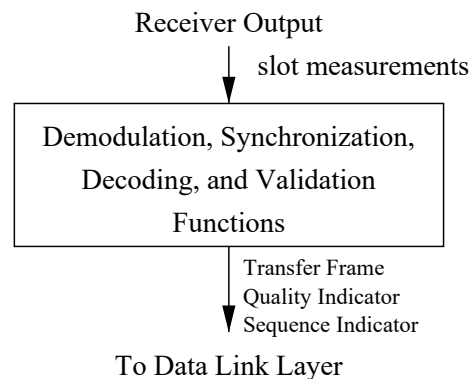


Figure 2-4: Internal Organization of Telemetry Signaling at the Receiving End

For HPE, at the receiving end, the Coding and Synchronization Sublayer accepts receiver outputs from the Physical Layer and performs functions selected for the mission. The receiver outputs are slot measurements, which are receiver estimates of the intensity of light, number of photons observed, or related statistic, for each slot of the received transmission. Among these functions is codeword synchronization and Serially Concatenated convolutionally coded Pulse Position Modulation (SCPPM) decoding, from which Synchronization-Marked Transfer Frames (SMTFs) are recovered. Synchronization Markers present in the SMTF allow synchronization and recovery of each transfer frame, which is delivered to the Data Link Protocol Sublayer along with a quality indicator and sequence indication.

For O3K, at the receiving end, the Coding and Synchronization Sublayer accepts receiver outputs from the Physical Layer and performs functions selected for the mission. Among these functions is frame synchronization and Forward Error Correction decoding, from which SMTFs are recovered if frame adaptation was performed. Synchronization markers present in

the SMTF allow synchronization and recovery of each transfer frame, which is delivered to the Data Link Protocol Sublayer along with a quality indicator and sequence indication.

2.3.2.2 Telemetry Transfer Frame Validation

After decoding and transfer frame recovery is performed, the upper layers at the receiving end also need to know whether or not each recovered transfer frame can be used as a valid data unit; that is, an indication of the quality of the received frame is needed. This function is called transfer frame validation and produces the quality indicator.

The decoder can determine, with a very high probability, whether or not each codeword was correctly decoded. Any transfer frames that are recovered from only correctly decoded codewords are marked valid; transfer frames recovered from one or more incorrectly decoded codewords are marked invalid.

NOTE – The Frame Error Control Field defined in reference [1], [2], or [8] may also be used for additional frame validation in the Data Link Protocol Sublayer.

2.3.2.3 Synchronization

This Recommended Standard specifies a method for synchronizing telemetry transfer frames using an Attached Synchronization Marker (ASM) (see Attached synchronization marker Figure 2-5).

2.4 INTERNAL ORGANIZATION OF BEACON AND AOS/USLP TRANSFER FRAME SIGNALING AT THE CODING AND SYNCHRONIZATION SUBLAYER

2.4.1 INTRODUCTION

This subsection applies only to HPE, as the beacon is not modulated with data for O3K.

2.4.2 AOS/USLP TRANSFER FRAME SIGNALING AT THE SENDING END

Figure 2-5 shows the internal organization of AOS/USLP transfer frame signaling at the Coding and Synchronization Sublayer of the sending end. This figure identifies functions

performed by the sublayer and shows logical relationships among these functions. The figure is not intended to imply any hardware or software configuration in a real system.

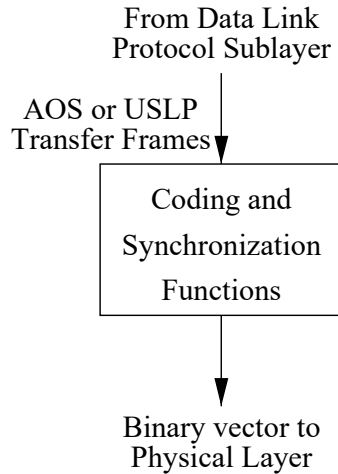


Figure 2-5: Internal Organization of AOS/USLP Transfer Frame Signaling at the Sending End

At the sending end, the Coding and Synchronization Sublayer accepts AOS or USLP fixed-length transfer frames from the Data Link Protocol Sublayer (see Figure 2-5). It then performs functions selected for the mission and generates Low-Density Parity-Check (LDPC) encoded Synchronization-Marked Codewords (SMCWs). These SMCWs are Pseudo-random Noise (PN)-spread and mapped into Binary Pulse Position Modulation (2-PPM) symbols. Two guard slots are inserted to the 2-PPM symbol stream and sent to the Physical Layer.

2.4.3 AOS/USLP TRANSFER FRAME SIGNALING AT THE RECEIVING END

2.4.3.1 General

Figure 2-6 shows the internal organization of AOS/USLP transfer frame signaling at the Coding and Synchronization Sublayer of the receiving end. This figure identifies functions performed by the sublayer and shows logical relationships among these functions. The figure is not intended to imply any hardware or software configuration in a real system.

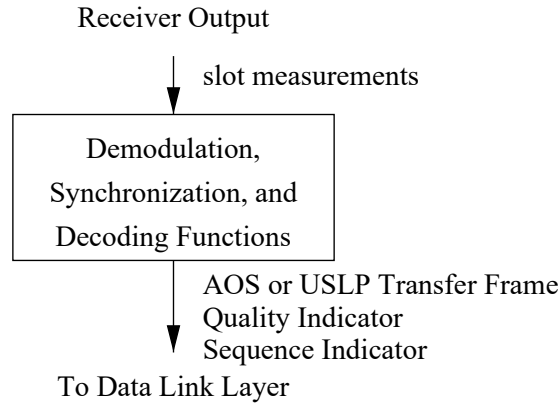


Figure 2-6: Internal Organization of the AOS/USLP Transfer Frame Signaling at the Receiving End

At the receiving end, the Coding and Synchronization Sublayer accepts receiver outputs from the Physical Layer and performs functions selected for the mission. Among these functions is 2-PPM, codeword synchronization, LDPC decoding, and AOS transfer frame synchronization. The recovered AOS/USLP transfer frames are recovered and delivered to the Data Link Protocol Sublayer, along with a quality indicator and sequence indication.

2.4.3.2 Synchronization

This Recommended Standard specifies a method for synchronizing transfer frames using an ASM (see 5.3).

3 HPE TELEMETRY SIGNALING

3.1 OVERVIEW

This Recommended Standard operates by taking CCSDS TM, AOS, or USLP transfer frames as input and producing a binary vector indicating the positions of pulsed slots as output to the Physical Layer.

No intervening slots (data or fill) are added to this output; if needed, zero fill is implemented at transmission closure by the Coding and Synchronization Sublayer. The functional blocks of the architecture at the sending end are shown in figure Figure 3-1, along with the notation used in the following subsections that defines these functions mathematically. It should be understood that the functions need not be implemented explicitly as defined here; any implementation producing the proper pattern of pulsed slots complies with the standard.

As shown in Figure 3-1, an ASM is prepended to each transfer frame. The bit pattern of the ASM and its insertion are defined in 4.3, and the data unit that consists of the ASM and the transfer frame is called the SMTF. The ASM will be eventually encoded. The stream of SMTFs is sliced into Information Blocks that are pseudo-randomized. Then a CRC and termination bits are attached to each pseudo-randomized information block and provided as input to the Channel Encoder, as described later in this section. The PPM symbols composing the generated codewords are interleaved, and a Codeword Synchronization Marker (CSM) is prepended to each interleaved codeword, as shown in Figure 3-1. Each PPM symbol is then repeated and mapped into a binary vector of length M to generate a vector of pulsed slots. Guard slots are inserted after each PPM symbol.

At the receiving end on the ground, two levels of synchronization are required: codeword synchronization (identified by the CSM) and transfer frame synchronization (identified by the ASM). CSM Synchronization is achieved by recognizing the specific bit pattern of the CSM in the symbol stream. This synchronization is then verified by making further checks. The codeword and codeword lengths are fixed and managed for a given phase of a mission. The detailed steps are described in the next subsections. Details of much of the theory and implementation are described in informative reference [G1].

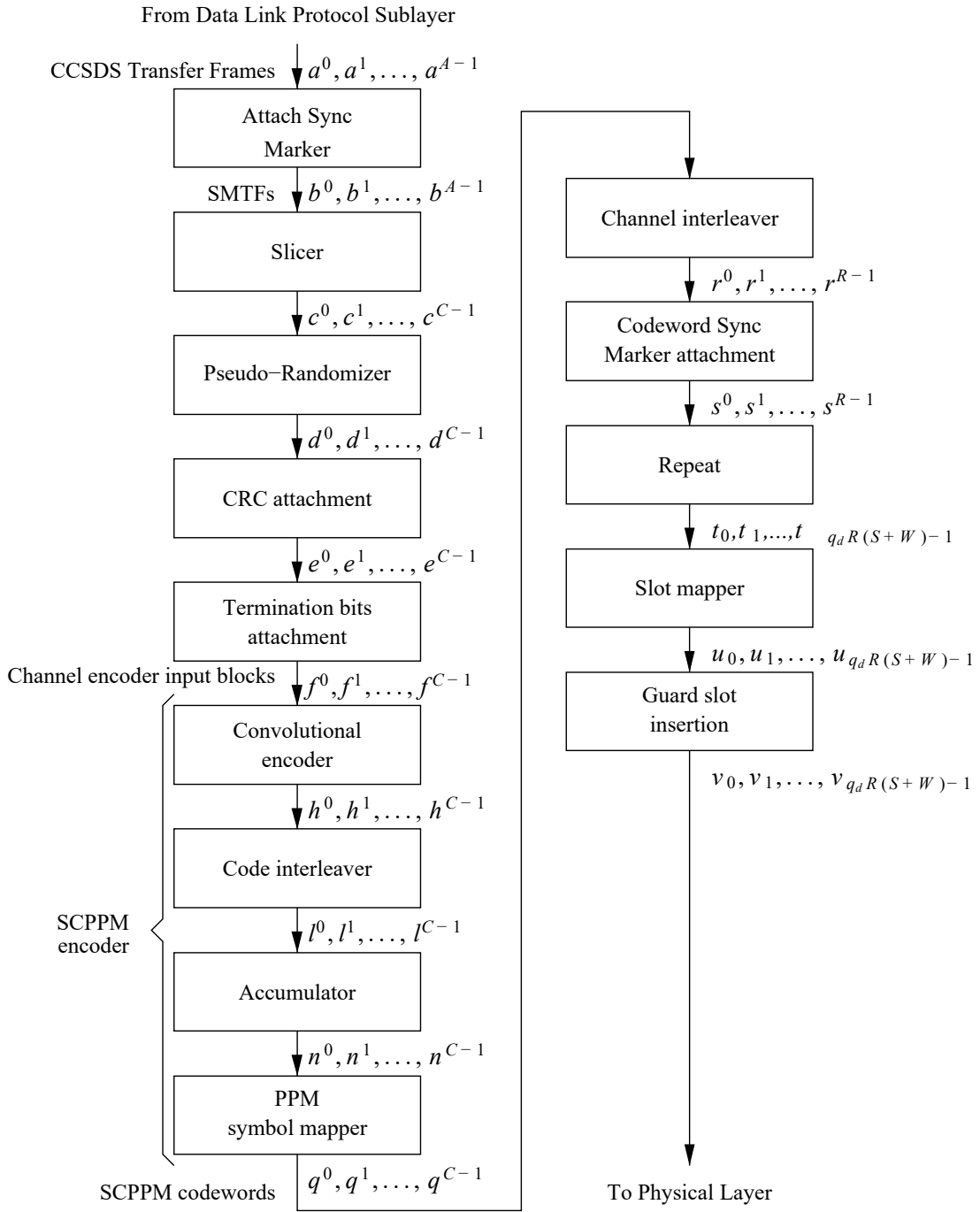


Figure 3-1: Functional Diagram for Telemetry Signaling

3.2 CCSDS TRANSFER FRAMES

The input to the Coding and Synchronization Sublayer shall be a sequence of CCSDS transfer frames with structure as defined in references [1], [2], and [8], and is denoted by

$$\mathbf{a}^0, \mathbf{a}^1, \dots, \mathbf{a}^{A-1},$$

and for $i \in \{0, 1, \dots, A-1\}$, the i^{th} transfer frame is denoted

$$\mathbf{a}^i = a_0^i, a_1^i, \dots, a_{T_i-1}^i,$$

where $a_j^i \in \{0,1\}$ is the j^{th} bit of the i^{th} frame, and T_i is the number of bits in the i^{th} frame.

NOTE – The encoding described in this subsection may be performed in a streaming fashion; that is, not all A transfer frames of a full communications session need be available at the time encoding is begun, and the value of A need not be known a priori.

3.3 ATTACHED SYNCHRONIZATION MARKER

3.3.1 ATTACHMENT METHOD

A 32-binary-digit ASM shall be prepended to each transfer frame, resulting in an SMTF, as follows: for $i \in \{0, 1, \dots, A-1\}$ the i^{th} SMTF is denoted

$$\mathbf{b}^i = b_0^i, b_1^i, \dots, b_{B_i-1}^i,$$

where $B_i = T_i + 32$ and

$$b_j^i = \begin{cases} s_j, & \text{if } 0 \leq j < 32 \\ a_{j-32}^i, & \text{if } 32 \leq j < B_i \end{cases}.$$

NOTE – Construction of SMTFs is shown in Figure 3-2.

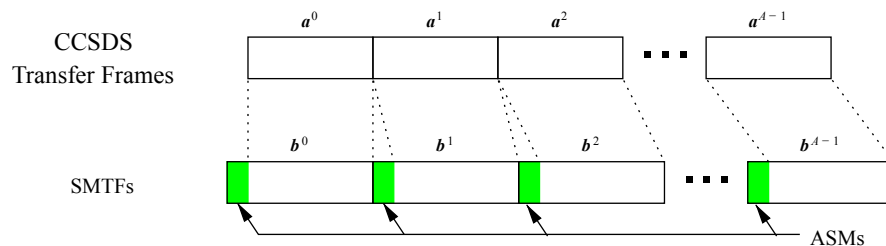


Figure 3-2: ASM Attachment

3.3.2 SEQUENCE SPECIFICATION

The ASM shall be the sequence $s = s_0, s_1, \dots, s_{31}$, represented in hexadecimal notation as

$$s = 1ACFFC1D.$$

3.4 SLICER

3.4.1 SLICING LENGTH

The sequence of SMTFs shall be sliced into information blocks of length k , where k is determined by the code rate $r \in \{1/3, 1/2, 2/3\}$ (see Table 3-1: Information Block Sizes), which is a managed parameter. The last information block may be less than k .

Table 3-1: Information Block Sizes

Code Rate	Information block size	Length of information blocks with CRC-32 and 2-binary-digit termination added
r	k	\hat{k}
1/3	5006	5040
1/2	7526	7560
2/3	10046	10080

3.4.2 ZERO FILL

3.4.2.1 Slicer Output

3.4.2.1.1 At transmission closure, the slicer output shall be zero-filled with the minimum number of ‘zeroes’ so that its length is a multiple of k .

NOTES

- The slicing and zero fill is as shown in Figure 3-3 and described herein.

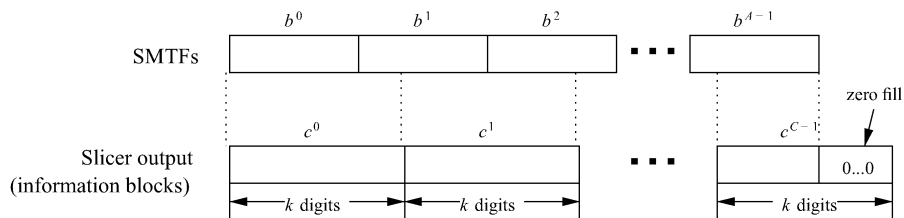


Figure 3-3: Slicer

- The sequence of SMTFs,

$$\mathbf{b}^0, \mathbf{b}^1, \dots, \mathbf{b}^{A-1},$$

is a vector of vectors that can be viewed as a single vector with binary digits in the same order,

$$\hat{\mathbf{b}} = \hat{b}_0, \hat{b}_1, \dots, \hat{b}_{B-1},$$

where $B = \sum_{i=0}^{A-1} B_i$. The sequence $\hat{\mathbf{b}}$ is filled at its end with the minimum number of ‘zeroes’ so that its length is a multiple of k . The sequence $\tilde{\mathbf{b}}$ is denoted by

$$\tilde{\mathbf{b}} = \hat{b}_0, \hat{b}_1, \dots, \hat{b}_{B-1}, \underbrace{0, 0, \dots, 0}_P,$$

where

$$\tilde{b}_i = \begin{cases} \hat{b}_i, & \text{if } 0 \leq i < B \\ 0, & \text{if } B \leq i < B + P \end{cases}$$

and

$$P = \min\{p : k \mid B + p\}.$$

3.4.2.1.2 The slicer shall reindex $\tilde{\mathbf{b}}$ into $C = (B + P)/k$ blocks each of length k :

$$\mathbf{c}^0, \mathbf{c}^1, \dots, \mathbf{c}^{C-1},$$

where for $i \in \{0, 1, \dots, C - 1\}$ the i^{th} block is denoted $\mathbf{c}^i = c_0^i, c_1^i, \dots, c_{k-1}^i$, and for $j \in \{0, 1, \dots, k - 1\}$ the j^{th} symbol of the i^{th} block is

$$c_j^i = \tilde{b}_{ki+j}.$$

3.5 PSEUDO-RANDOMIZER

3.5.1 DESCRIPTION

Each k -digit information block from the slicer shall be pseudo-randomized by performing the digit-wise modulo-2 addition with a pseudo-random sequence, as shown in Figure 3-4 and described herein.

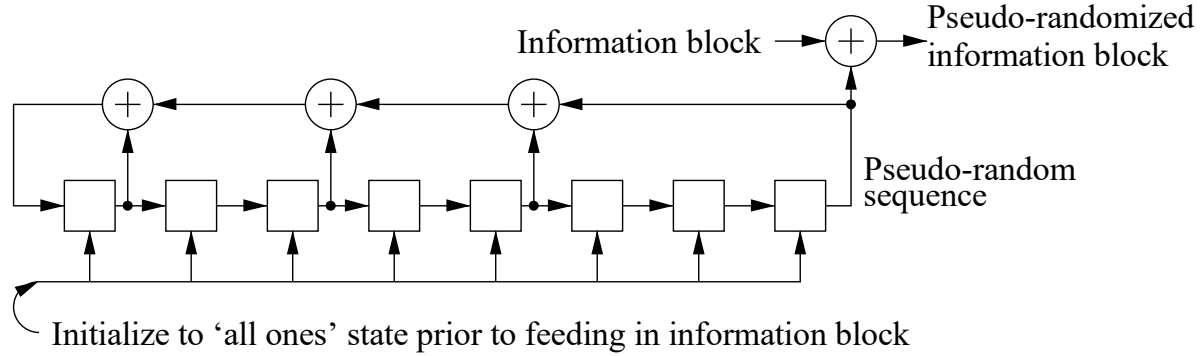


Figure 3-4: Pseudo-Randomizer Data Flow

3.5.1.1 For $i \in \{0, 1, \dots, C - 1\}$, the i^{th} pseudo-randomized information block is denoted $\mathbf{d}^i = d_0^i, d_1^i, \dots, d_{k-1}^i$ where for $j \in \{0, 1, \dots, k - 1\}$, the j^{th} symbol of the i^{th} block is

$$d_j^i = c_j^i \oplus p_j,$$

where \oplus represents modulo-2 addition and p_j is defined in 3.5.2.

3.5.2 SEQUENCE SPECIFICATION

The pseudo-random sequence p_0, p_1, \dots, p_{k-1} shall be generated by the polynomial:

$$g(D) = D^8 + D^7 + D^5 + D^3 + 1.$$

NOTE – The sequence is the same one specified in reference [7]. It is periodic with period 255. The first 40 binary digits of the pseudo-random sequence are:

$$p_0, p_1, \dots, p_{39} = 1111 1111 0100 1000 0000 1110 1100 0000 1001 1010$$

3.5.3 SEQUENCE INITIALIZATION

3.5.3.1 This sequence shall begin at the first digit of the information block and shall repeat after 255 binary digits, continuing repeatedly until the end of the information block.

3.5.3.2 The sequence generator shall be initialized to the 'all ones' state at the start of each information block.

3.6 CRC ATTACHMENT

3.6.1 DESCRIPTION

3.6.1.1 Thirty-two CRC binary digits shall be appended to the end of each pseudo-randomized information block as shown in Figure 3-5 and described herein.

3.6.1.2 The i^{th} pseudo-randomized information block with the attached CRC shall be $e^i = e_0^i, e_1^i, \dots, e_{k+31}^i$, where

$$e_j^i = \begin{cases} d_j^i, & \text{if } 0 \leq j < k \\ z_{j-k}^i, & \text{if } k \leq j < k + 32 \end{cases},$$

where z_{j-k}^i is defined in 3.6.2.

3.6.2 CRC SEQUENCE SPECIFICATION

3.6.2.1 The CRC parity binary digits shall be computed as follows: for $i \in \{0, 1, \dots, C-1\}$, the i^{th} pseudo-randomized information block d^i is padded with 32 ‘zeroes’ and expressed in polynomial notation as

$$d^i(X) = d_0^i X^{k+31} + d_1^i X^{k+30} + \dots + d_{k-2}^i X^{33} + d_{k-1}^i X^{32}.$$

3.6.2.2 The polynomial notation for the thirty-two binary digit CRC is

$$z^i(X) = z_0^i X^{31} + z_1^i X^{30} + \dots + z_{30}^i X + z_{31}^i$$

and is given by

$$z^i(X) = \left[d^i(X) + \sum_{j=0}^{31} X^{k+j} \right] \text{mod } h(X),$$

where all arithmetic is modulo 2 and $h(X)$ is the generator polynomial given by

$$h(X) = X^{32} + X^{29} + X^{18} + X^{14} + X^3 + 1.$$

NOTES

- 1 In the expression, for $z^i(X)$ the $\sum_{j=0}^{31} X^{k+j}$ term has the effect of presetting the shift registers to ‘all ones’ prior to encoding.
- 2 A possible technique for generating the CRC binary digits is given in Figure 3-5. For each pseudo-randomized information block, the shift register cells are initialized to ‘1’. The ganged switch is in position (1) while the k pseudo-randomized information digits are being transferred and in position (2) for the thirty-two CRC digits.

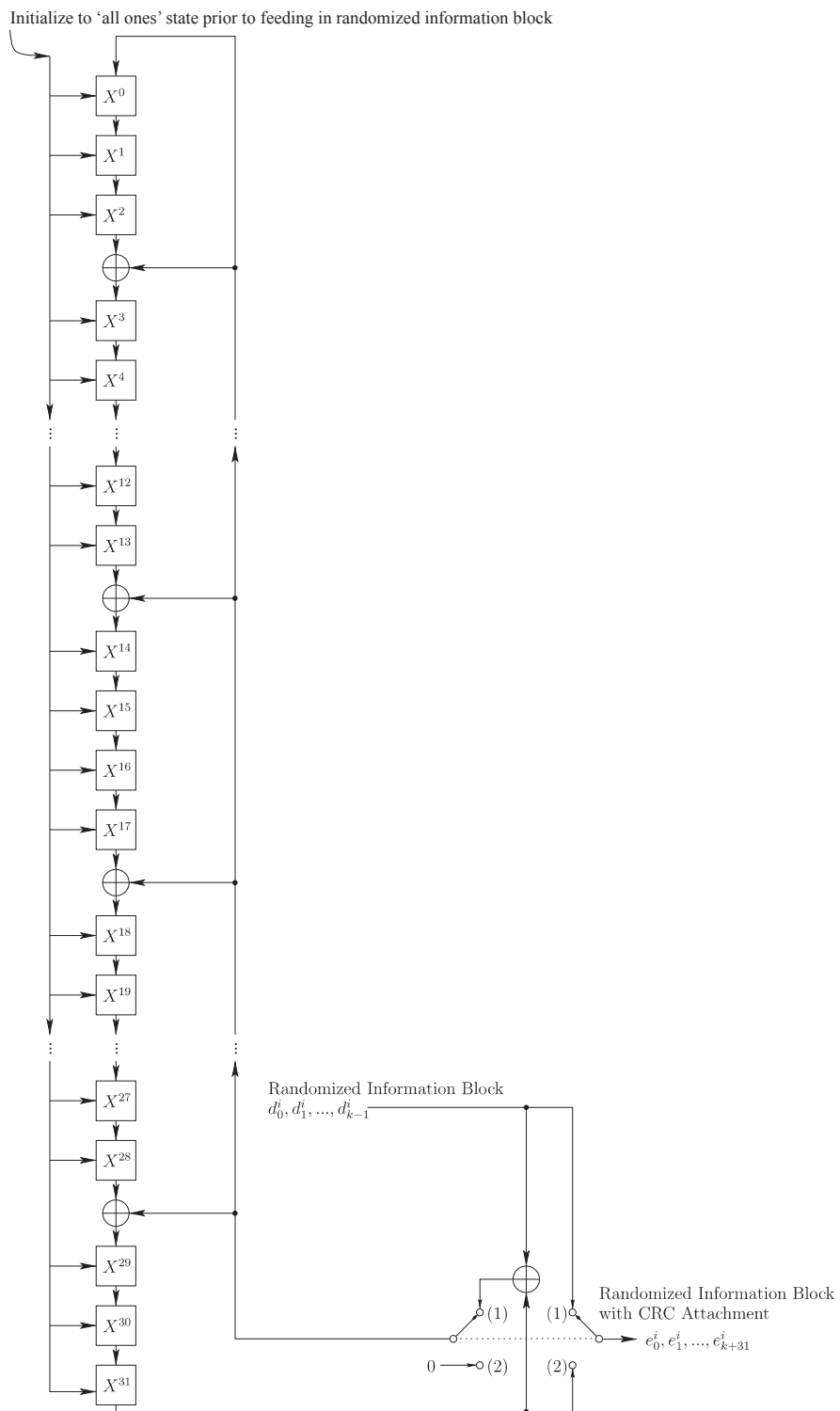


Figure 3-5: Shift Register Implementation of CRC Attachment

3.7 TERMINATION BINARY DIGITS ATTACHMENT

Two ‘zeroes’ shall be appended to each pseudo-randomized information block with attached CRC to produce a block of $\hat{k} = k + 34$ binary digits as follows: for $i \in \{0, 1, \dots, C - 1\}$, the i^{th} SCPPM encoder input block is $\mathbf{f}^i = f_0^i, f_1^i, \dots, f_{\hat{k}-1}^i$, where

$$f_j^i = \begin{cases} e_j^i, & \text{if } 0 \leq j < \hat{k} - 2 \\ 0, & \text{if } j = \hat{k} - 2 \text{ or } j = \hat{k} - 1 \end{cases}.$$

NOTES

- 1 This block is called the SCPPM encoder input block.
- 2 The pseudo-randomized information block with CRC and termination binary digits is shown in Figure 3-6.

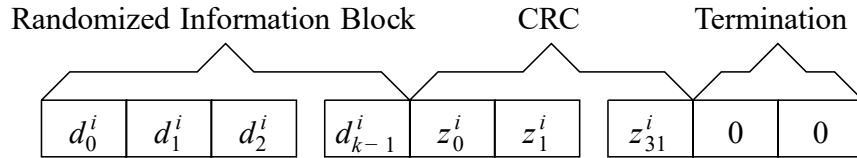


Figure 3-6: SCPPM Encoder Input Block

3.8 SCPPM ENCODER

3.8.1 OVERVIEW

The SCPPM encoder has the structure shown in Figure 3-7. Each SCPPM encoder input block has length $\hat{k} = 15120r$ (see Table 4-1), and the outer encoder produces 15120 convolutionally coded binary symbols (i.e., code digits), which are interleaved, accumulated, and mapped to PPM symbols. The individual SCPPM encoder components are described in the following subsections.

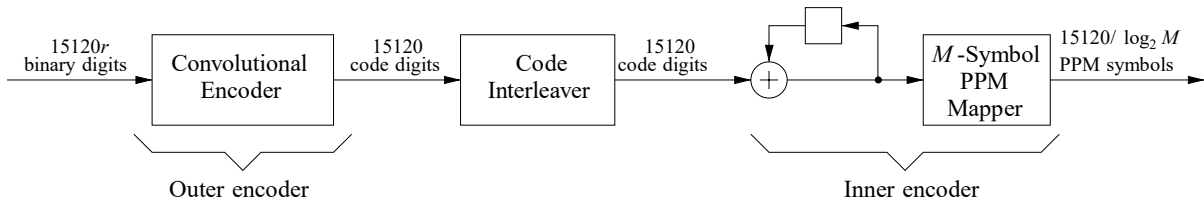


Figure 3-7: SCPPM Encoder

3.8.2 CONVOLUTIONAL ENCODER

3.8.2.1 Generator Polynomial

The SCPPM outer code shall be a constraint-length-three convolutional code defined by the generator polynomials

$$\begin{aligned} g^{(1)}(D) &= 1 + D^2 \\ g^{(2)}(D) &= 1 + D + D^2 \\ g^{(3)}(D) &= 1 + D + D^2, \end{aligned}$$

or [5, 7, 7] in octal notation.

NOTES

- 1 The encoder for this rate 1/3 mother code is shown in Figure 3-8.

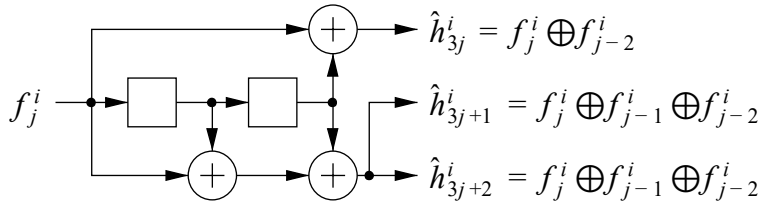


Figure 3-8: Encoder for Rate 1/3 Mother Convolutional Encoder

- 2 The j^{th} binary digit of the i^{th} SCPPM encoder input block, f_j^i , enters the convolutional encoder, which in response produces the three code symbols $\hat{h}_{3j}^i, \hat{h}_{3j+1}^i, \hat{h}_{3j+2}^i$ corresponding to the polynomials $g^{(1)}(D), g^{(2)}(D), g^{(3)}(D)$, respectively. After all \hat{k} binary digits of f^i enter the encoder, the encoder has produced the convolutional codeword

$$\hat{\mathbf{h}}^i = \hat{h}_0^i, \hat{h}_1^i, \dots, \hat{h}_{3\hat{k}-1}^i.$$

3.8.2.2 Initialization

The encoder shall be initialized to the ‘all zeroes’ state prior to encoding each input block.

NOTE – For each input block after the first, the initialization to ‘all zeroes’ happens naturally because of the termination digits used in the preceding input block.

3.8.2.3 Puncturing

3.8.2.3.1 The rate 1/3 code may be punctured, resulting in a rate 1/2 or rate 2/3 code, using the puncture patterns given in table Table 3-2: Convolutional Encoder Puncture Patterns

Table 3-2: Convolutional Encoder Puncture Patterns

Rate	P_0	P_1	P_2	P_3	P_4	P_5
1/3	1	1	1	1	1	1
1/2	1	1	0	1	1	0
2/3	1	1	0	0	1	0

3.8.2.3.2 The puncturing shall be accomplished using the following procedure:

```

j ← 0
for m ← 0 to 3  $\hat{k}$  - 1
    if ( $P_{m \bmod 6} \equiv 1$ )
         $h_j^i \leftarrow \hat{h}_m^i$ 
        j ← j + 1
    endif
endfor

```

NOTES

1 The rate 1/2 code punctures every 3rd code symbol \hat{h}_{3j+2}^i :

$$\hat{h}_0^i, \hat{h}_1^i, \hat{h}_2^i, \hat{h}_3^i, \hat{h}_4^i, \hat{h}_5^i, \dots, \hat{h}_{3\hat{k}-6}^i, \hat{h}_{3\hat{k}-5}^i, \hat{h}_{3\hat{k}-4}^i, \hat{h}_{3\hat{k}-3}^i, \hat{h}_{3\hat{k}-2}^i, \hat{h}_{3\hat{k}-1}^i.$$

The rate 2/3 code additionally punctures every other first code symbol \hat{h}_{6j+3}^i :

$$\hat{h}_0^i, \hat{h}_1^i, \hat{h}_2^i, \hat{h}_3^i, \hat{h}_4^i, \hat{h}_5^i, \dots, \hat{h}_{3\hat{k}-6}^i, \hat{h}_{3\hat{k}-5}^i, \hat{h}_{3\hat{k}-4}^i, \hat{h}_{3\hat{k}-3}^i, \hat{h}_{3\hat{k}-2}^i, \hat{h}_{3\hat{k}-1}^i.$$

The resulting i^{th} convolutional codeword is denoted

$$\mathbf{h}^i = h_0^i, h_1^i, \dots, h_{15119}^i.$$

2 \hat{k} is defined so that, regardless of rate, each convolutional codeword has length 15120.

3.8.3 CODE INTERLEAVER

3.8.3.1 The binary symbols of each 15120-symbol convolutional codeword shall be permuted by a 15120-binary-symbol block interleaver as follows: for $i \in \{0, 1, \dots, C-1\}$, the i^{th} interleaved codeword is denoted $\mathbf{l}^i = l_0^i, l_1^i, \dots, l_{15119}^i$

3.8.3.2 For $j \in \{0, 1, \dots, 15119\}$, the j^{th} code symbol of the i^{th} interleaved codeword shall be

$$l_j^i = h_{\pi(j)}^i,$$

where

$$\pi(j) = (11j + 210j^2) \bmod 15120.$$

3.8.3.3 The interleaver may be implemented by writing code symbols sequentially to positions 0 through 15119 and by reading code symbols in interleaved order from positions $\pi(0)$ through $\pi(15119)$.

NOTE – The code interleaver is part of the SCPPM encoder and is not related to the channel interleaver.

3.8.4 ACCUMULATOR

3.8.4.1 Interleaved convolutional codewords shall enter an accumulator, a rate-one code with transfer function $1 / (1 + D)$, as shown in Figure 3-9.

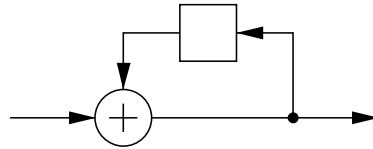


Figure 3-9: The Accumulator

3.8.4.2 The accumulator shall be initialized to the ‘all zeroes’ state prior to encoding each interleaved convolutional codeword as follows: for the i^{th} codeword, the j^{th} output of the accumulator is given by

$$n_j^i = \begin{cases} l_j^i, & \text{if } j = 0 \\ n_{j-1}^i \oplus l_j^i, & \text{if } 1 \leq j < 15120 \end{cases}.$$

3.8.5 PPM SYMBOL MAPPER

The output code symbols from the accumulator shall be mapped to M -ary PPM symbols, where $M \in \{4, 8, 16, 32, 64, 128, 256\}$ is a managed parameter, as follows:

- a) Every $m = \log_2 M$ binary code symbols shall be grouped to form one PPM symbol, which is an integer in $\{0, 1, \dots, M - 1\}$.

NOTE – The output is the sequence of SCPPM codewords q^0, q^1, \dots, q^{C-1} . Each codeword consists of

$$S = \frac{15120}{m}$$

PPM symbols.

b) For $j \in \{0, 1, \dots, S - 1\}$, the j^{th} PPM symbol of the i^{th} SCPPM codeword shall be

$$q_j^i = \sum_{a=0}^{m-1} 2^{m-a-1} \cdot n_{mj+a}^i.$$

NOTE – In this way, the PPM symbol is simply the integer value corresponding to each grouping of m binary code symbols.

For example, when $M = 16$, an accumulator output of

$$n_0^i, n_1^i, n_2^i, n_3^i, n_4^i, n_5^i, n_6^i, n_7^i, \dots = \underbrace{1, 1, 0, 1, 0, 1, 0, 1, \dots}_{13}, \dots$$

would correspond to PPM symbols $q_0^i, q_1^i, \dots = 13, 5, \dots$

Since 15120 is a multiple of $m = \log_2 M$, there are no leftover code symbols in the groupings.

3.9 CHANNEL INTERLEAVER

3.9.1 GENERAL

The sequence of PPM symbols \hat{q} shall be channel interleaved with a convolutional interleaver, as shown in Figure 3-10 and described herein.

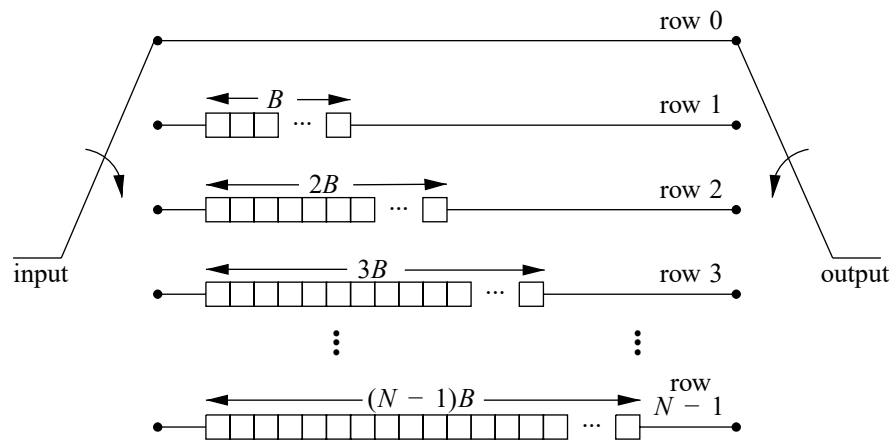


Figure 3-10: Interleaver

3.9.2 CHANNEL INTERLEAVER INPUT NOTATION

The sequence of SCPPM codewords $\mathbf{q} = \mathbf{q}^0, \mathbf{q}^1, \dots, \mathbf{q}^{C-1}$ shall be a vector of vectors that can be viewed as a single vector of PPM symbols

$$\hat{\mathbf{q}} = \hat{q}_0, \hat{q}_1, \dots, \hat{q}_{CS-1},$$

where for $i \in \{0, 1, \dots, C-1\}$ and $j \in \{0, 1, \dots, S-1\}$,

$$\hat{q}_{iS+j} = q_j^i.$$

3.9.3 CHANNEL INTERLEAVER PARAMETERS

Parameters N and B are managed and shall be chosen so that BN is a multiple of S , which in turn is a multiple of N .

NOTE – The interleaver has N rows, with the i^{th} row containing a shift register of length iB , meaning that it holds iB PPM symbols.

3.9.4 CHANNEL INTERLEAVER INITIALIZATION

Prior to channel interleaving, the shift registers shown in Figure 3-10 may be in any state.

3.9.5 CHANNEL INTERLEAVER OPERATION

3.9.5.1 Channel Interleaver Arm Positions

3.9.5.1.1 The input PPM symbols $\hat{\mathbf{q}}$ shall be demultiplexed into the N rows, sequentially and in circular fashion, beginning with row 0.

3.9.5.1.2 The outputs of the N shift registers shall be multiplexed, sequentially and in circular fashion, beginning with row 0.

3.9.5.1.3 During each step of the operation of the channel interleaver, the demultiplexer arm shall be positioned at the same row as the multiplexer arm.

3.9.5.1.4 The i^{th} interleaver output is

$$\hat{r}_i = \hat{q}_{\sigma(i)},$$

where $\sigma(i)$ is defined recursively by

$$\sigma(i) = \begin{cases} i, & \text{if } i \bmod N = 0 \\ \sigma(i-1) - NB + 1, & \text{otherwise} \end{cases}.$$

NOTES

- 1 Negative values of $\sigma(i)$ refer to initial interleaver register contents, and values of $\sigma(i)$ greater than $CS - 1$ refer to terminal register contents. In these cases, \hat{r}_i may be any value.
- 2 For example, when $N = 4$ and $B = 1$, the input $\hat{q}_0, \hat{q}_1, \hat{q}_2, \dots$ will produce an interleaver output of:

$$\hat{r}_0, \hat{r}_1, \hat{r}_2, \hat{r}_3, \hat{r}_4, \hat{r}_5, \hat{r}_6, \hat{r}_7, \hat{r}_8, \hat{r}_9, \hat{r}_{10}, \dots = \hat{q}_0, \hat{q}_{-3}, \hat{q}_{-6}, \hat{q}_{-9}, \hat{q}_4, \hat{q}_1, \hat{q}_{-2}, \hat{q}_{-5}, \hat{q}_8, \hat{q}_5, \hat{q}_2, \dots$$

3.9.5.2 Completion of the Channel Interleaver Operations

3.9.5.2.1 After the last symbol, \hat{q}_{CS-1} , is input, the interleaver shall be operated another $BN(N - 1)$ steps before \hat{q}_{CS-1} appears at the output.

NOTE – Thus the output contains $BN(N - 1)$ more symbols than the input. This output of the channel interleaver is

$$\hat{\mathbf{r}} = \hat{r}_0, \hat{r}_1, \dots, \hat{r}_{SC+BN(N-1)-1}$$

3.9.5.2.2 For $i \in \{0, 1, \dots, SC + BN(N - 1) - 1\}$, the i^{th} interleaver output shall be as defined in 3.9.5.1.

3.9.5.3 Reindexing Channel Interleaver Output to Form Interleaved Codewords

The sequence $\hat{\mathbf{r}}$ may be reindexed into $R = C + BN(N - 1) / S$ blocks each containing S symbols:

$$\mathbf{r}^0, \mathbf{r}^1, \dots, \mathbf{r}^{R-1},$$

where for $i \in \{0, 1, \dots, R - 1\}$ the i^{th} block is denoted $\mathbf{r}^i = r_0^i, r_1^i, \dots, r_{S-1}^i$, and for $j \in \{0, 1, \dots, S - 1\}$,

$$r_j^i = \hat{r}_{iS+j}.$$

NOTES

- 1 Each \mathbf{r}^i is called an interleaved codeword (notwithstanding the fact that it contains symbols from many different SCPPM codewords), because it contains S M -ary PPM symbols.
- 2 Since $BN(N - 1)$ is a multiple of S , there are no leftover PPM symbols in the last block.

3.10 CODEWORD SYNCHRONIZATION MARKER

3.10.1 DESCRIPTION

A CSM of W PPM symbols shall be prepended to each interleaved SCPPM codeword. After CSM attachment, the j^{th} PPM symbol of the i^{th} interleaved codeword is:

$$\hat{s}_j^i = \begin{cases} w_j, & \text{if } 0 \leq j < W \\ r_{j-W}^i, & \text{if } W \leq j < S + W \end{cases}$$

NOTE – The sequence of CSM+interleaved-codewords is $\hat{\mathbf{s}} = \hat{\mathbf{s}}^0, \hat{\mathbf{s}}^1, \dots, \hat{\mathbf{s}}^{R-1}$

3.10.2 CSM SPECIFICATION

3.10.2.1 For $M \geq 8$, W shall be 16; for $M = 4$, W shall be 24. The CSM shall be:

$$\mathbf{w} = \begin{cases} (0, 3, 1, 2, 1, 3, 2, 0, 0, 3, 2, 1, 0, 2, 1, 3, 1, 0, 3, 2, 3, 2, 1, 0) & \text{if } M = 4 \\ (0, 3, 1, 2, 5, 4, 7, 6, 6, 7, 4, 5, 2, 1, 3, 0) & \text{if } M = 8 \\ (0, 2, 7, 14, 1, 2, 15, 5, 8, 4, 10, 2, 14, 3, 14, 11) & \text{if } M \geq 16 \end{cases}$$

3.10.2.2 This sequence shall be reindexed in PPM symbols as $\mathbf{s} = s_0, s_1, \dots, s_{R(S+W)-1}$, where

$$s_{iS+j} = \hat{s}_j^i.$$

3.11 REPEAT

Each PPM symbol shall be repeated so that it appears q_d times, where the repeat factor $q_d \in \{1, 2, 3, 4, 8, 16, 32\}$ is a managed parameter, as follows: for $j \in \{0, 1, \dots, q_d R(S+W) - 1\}$, the j^{th} symbol at the output of the repeater is

$$t_j = s_{\lfloor j/q_d \rfloor} \bmod M,$$

where $\lfloor x \rfloor$ denotes the integer part of x .

3.12 SLOT MAPPER

For $j \in \{0, 1, \dots, q_d R(S+W) - 1\}$, the j^{th} repeated PPM symbol $t_j \in \{0, 1, \dots, M-1\}$ shall be mapped to a binary vector of length M ,

$$\mathbf{u}_j = u_{j,0}, u_{j,1}, \dots, u_{j,M-1},$$

where for $i \in \{0, 1, \dots, M-1\}$,

$$u_{j,i} = \begin{cases} 1, & \text{if } i = t_j \\ 0, & \text{otherwise} \end{cases}$$

NOTE – Each \mathbf{u}_j contains $M-1$ ‘zeroes’ and one ‘one’.

3.13 GUARD SLOT INSERTION

After each set of M slots \mathbf{u}_j , $M/4$ guard slots shall be inserted, as shown in Figure 3-11. The result is the slot sequence $\mathbf{v} = \mathbf{v}_0, \mathbf{v}_1, \dots, \mathbf{v}_{q_d R(S+W)-1}$, where each \mathbf{v}_j is a vector of length $5M/4$, and

$$\mathbf{v}_{j,i} = \begin{cases} \mathbf{u}_{j,i}, & \text{if } 0 \leq i < M \\ 0, & \text{if } M \leq i < 5M/4 \end{cases}$$

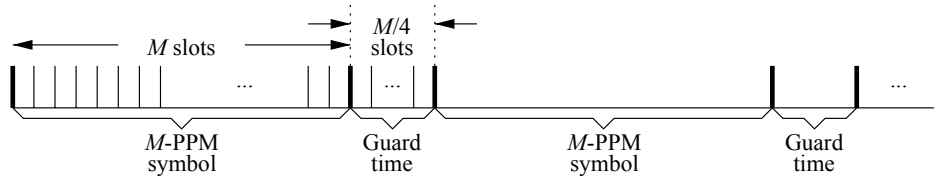


Figure 3-11: Guard Slot Insertion

3.14 TRANSFER FRAME VALIDATION

3.14.1 OVERVIEW

The receiving end of the telemetry transmission obtains slot and symbol synchronization, then uses the CSMs to obtain codeword synchronization. The symbols are deinterleaved, and SCPPM codewords are decoded and derandomized. The resulting sequence of SMTFs are synchronized by identifying the ASMs, which are removed, yielding the transfer frames.

3.14.2 QUALITY INDICATOR

A transfer frame shall be marked valid if it is recovered from one or more correctly decoded SCPPM codewords; a transfer frame shall be marked invalid if it is recovered from one or more incorrectly decoded codewords.

3.14.3 OPTIONAL FRAME ERROR CONTROL FIELD IN TRANSFER FRAME

The Frame Error Control Field (FECEF) defined in reference [1], [2], or [8] is optional, and the system designer may choose to use it for additional frame validation in the Data Link Protocol Sublayer.

3.15 SEQUENCE INDICATOR

A Sequence Indicator shall be 'zero' when a transfer frame is the direct successor of the previous one, and 'one' when a gap has been detected.

4 O3K TELEMETRY SIGNALING

4.1 OVERVIEW

This Recommended Standard operates by taking CCSDS TM, AOS, or USLP transfer frames as input and producing a binary vector as output to the Physical Layer. The binary vector indicates which OOK symbols are to contain light pulses.

No intervening slots (data or fill) are added to this output. The functional blocks of the architecture at the sending end are shown in figure 4-1, along with the notation used in the following subsections that defines these functions mathematically. It should be understood that the functions need not be implemented explicitly as defined here; any implementation producing the proper binary vector complies with the standard.

As shown in figure 4-1, optional transfer frames adaptation is performed (4.3). An ASM is prepended to each transfer frame, forming an SMTF. The stream of SMTFs is sliced into information blocks (4.3.4) which are provided as input to the channel encoder (4.4).

Two different encoders are described, a Reed-Solomon (RS) (255,223) code (4.4.2) and rate 1/2 and 9/10 LDPC codes (4.4.3). Codewords are then channel interleaved (4.5), optionally repeated (4.6), and randomized (4.7).

The sync layer framing is performed (4.8): the sync layer frame (SLFRAME) header, composed of a sync marker and additional signaling fields (in-band signaling, interleaver signaling), is prepended to each major code frame. Each OOK symbol is then optionally repeated (4.9). The variation of the symbol rate provides another scheme to perform variation of the data rate.

At the receiving end, two levels of synchronization are required: SLFRAME synchronization (identified by the SLFRAME header sync marker) and transfer frame synchronization (identified by the ASM). SLFRAME synchronization is achieved by recognizing the specific symbol pattern of the header sync marker in the symbol stream. This synchronization is then verified by making further checks.

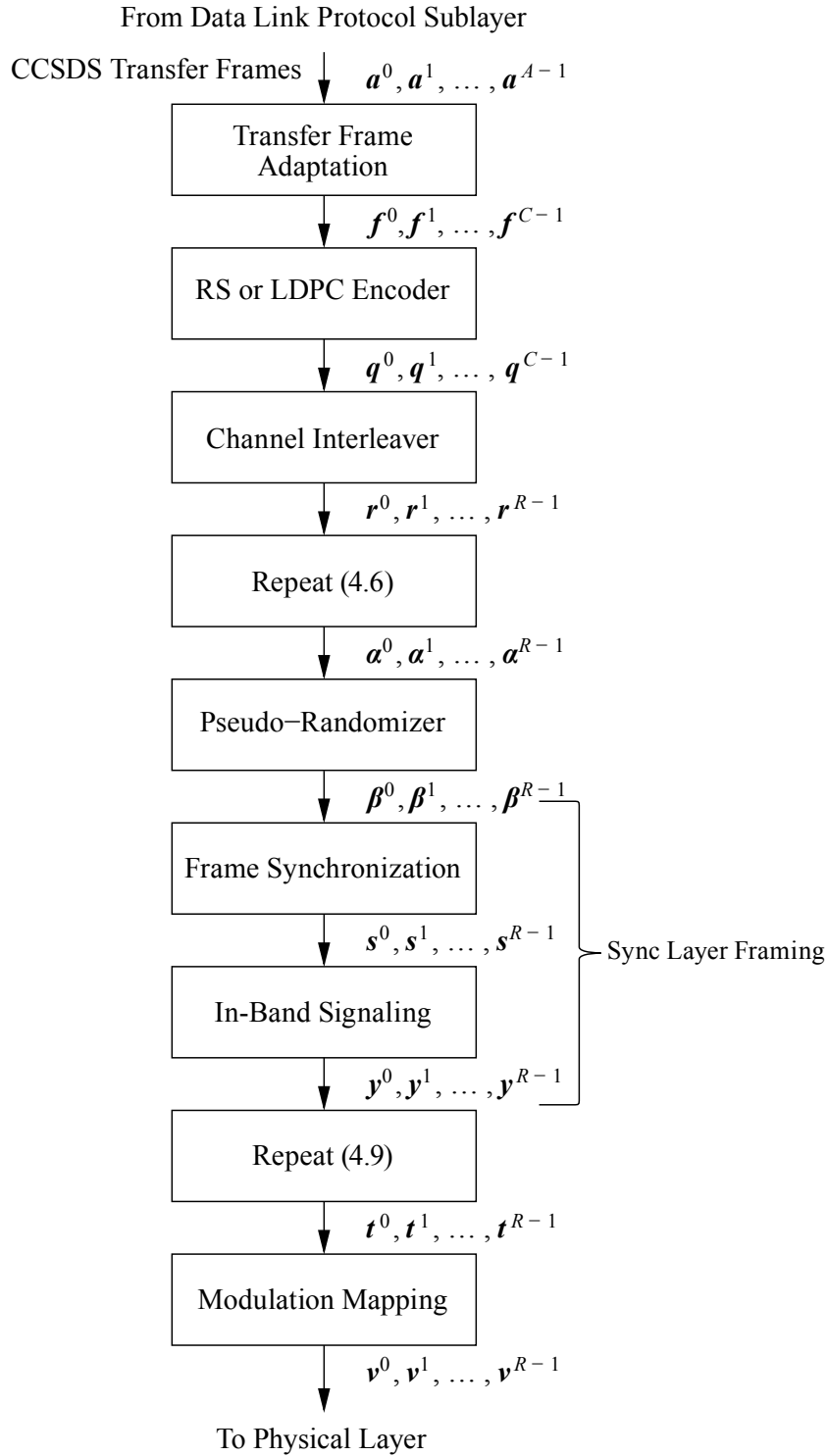


Figure 4-1: O3K Telemetry Signaling

NOTE – For each functional block, lower-case symbols identify the different types of input and output vectors, while upper-case letters represent the number of such vectors.

4.2 CCSDS TRANSFER FRAMES

The input to the Coding and Synchronization Sublayer shall be a sequence of CCSDS transfer frames as described in 3.2.

4.3 TRANSFER FRAME ADAPTATION

4.3.1 OVERVIEW

Transfer frame adaptation is the process of attaching synchronization markers and slicing the SMTF stream into blocks of an appropriate length for the channel encoder. This produces the correct information block size at the input of the encoder regardless the size of the input transfer frame. When the Data Link Protocol Sublayer provides transfer frames having a length corresponding to the information block size, transfer frame adaptation is not required.

4.3.2 USE OF TRANSFER FRAME ADAPTATION

Transfer frame adaptation may be used for O3K. A managed parameter indicates if transfer frame adaptation is used.

Notes:

- 1 TM [1], AOS [2], and USLP [8] are not designed with a dedicated transfer-frame synchronization mechanism. Hence, frame adaption is highly recommended for having frame synchronization based on the ASM;
- 2 Although not foreseen by CCSDS standards, if the data link layer provides a dedicated synchronization mechanism (e.g., generic frame procedure [G3]), then transfer frame adaption may be bypassed to avoid unnecessary overhead of the ASM.

4.3.3 ATTACHED SYNCHRONIZATION MARKER

If transfer frame adaptation is used, an ASM shall be prepended to each transfer frame, resulting in an SMTF, as described in 3.3.

4.3.4 SLICER

If transfer frame adaptation is used, the sequence of SMTFs shall be sliced into information blocks of length k , where k is determined by the code type and code rate (see Table 4-1), which are managed parameters. The size of the last information block may be less than k bits.

Table 4-1: Information Block Sizes.

Code Type	Code Rate r	Information block size in bits k
RS	223/255	1784
LDPC	1/2	15360
LDPC	9/10	27648

NOTE – The Data Link Protocol Sublayer ensures that the cumulative size of the transfer frames to be transmitted is compatible with the size of the encoder input.

4.4 CHANNEL CODING

4.4.1 GENERAL

Either an RS encoder or an LDPC encoder shall be used. A managed parameter indicates the code type to be used.

4.4.2 REED-SOLOMON ENCODER

When the RS encoder is used, codeblocks shall be computed from the input blocks as described in section 4 of reference [7], using parameters $E = 16$ and $Q = 0$, with the exception that the randomizer described in section 4.2.1 of [7] is not applied.

NOTES

- 1 $E = 16$ corresponds to the (255, 223) RS code.
- 2 $Q = 0$ means that a shortened codeblock is not used.
- 3 Reference [7] specifies symbol interleaving with allowed interleaving depths of $I = 1, 2, 3, 4, 5,$ and 8 .
- 4 The ASM described in subsection 4.3.10 of reference [7] is not attached to the codeblock.
- 5 The input to the encoder is I information blocks each of size $k = 1784$ bits (see table Table 4-1). The output codeblock has size $255 \times I \times 8$ binary digits. The maximum length is equal to $255 \times 8 \times 8 = 16320$ binary digits.
- 6 In the notation of figure Figure 4-1, the input blocks $\mathbf{f} = \mathbf{f}^0, \mathbf{f}^1, \dots, \mathbf{f}^{C-1}$, with $\mathbf{f}^i = f_0^i, f_1^i, \dots, f_{kI-1}^i$, are encoded and interleaved to produce codeblocks $\mathbf{q} = \mathbf{q}^0, \mathbf{q}^1, \dots, \mathbf{q}^{C-1}$, with $\mathbf{q}^i = q_0^i, q_1^i, \dots, q_{255 \times I \times 8}^i$.

4.4.3 LOW-DENSITY PARITY-CHECK ENCODER

4.4.3.1 Overview

This specification includes two Quasi-Cyclic (QC) LDPC codes:

- a rate 1/2 Protograph-Based Raptor-Like (PBRL) LDPC code;
- a rate 9/10 Accumulate-Repeat-Accumulate (ARA) LDPC code.

The input to the LDPC encoder is an information blocks of size $k = 15360$ bits for the rate 1/2 code and $k = 27648$ bits for the rate 9/10 code (see Table 4-1). For either rate code, the output codeword is $n = 30720$ binary digits. The code rate r is handled by an in-band signaling protocol. The codes are systematic before puncturing is applied and not systematic after puncturing, with the parity-check binary digits appended after the systematic bits.

A QC-LDPC code can be defined by a Parity-Check Matrix (PCM) of size $m_b L \times n_b L$. The PCM of a QC-LDPC will be denoted hereafter \mathbf{H} , and can be written as an $m_b \times n_b$ array

$$\mathbf{H} = \begin{pmatrix} \mathbf{h}_{0,0} & \cdots & \mathbf{h}_{0,n_b-1} \\ \vdots & \ddots & \vdots \\ \mathbf{h}_{m_b-1,0} & \cdots & \mathbf{h}_{m_b-1,n_b-1} \end{pmatrix}$$

where each submatrix $\mathbf{h}_{i,j}$, $0 \leq i \leq (m_b - 1)$, $0 \leq j \leq (n_b - 1)$, is an $L \times L$ circulant permutation matrix that can be described as an integer power α of the cyclic group generator \mathbf{Z} defined as

$$\mathbf{Z} = \begin{pmatrix} 0 & 1 & 0 & \cdots & \cdots & 0 \\ 0 & 0 & 1 & 0 & \ddots & \vdots \\ \vdots & & \ddots & \ddots & \ddots & \vdots \\ \vdots & & & & 1 & 0 \\ 0 & & & & 0 & 1 \\ 1 & 0 & \cdots & \cdots & \cdots & 0 \end{pmatrix}.$$

Thus, we have $\mathbf{h}_{i,j} = \mathbf{Z}^{\alpha_{i,j}}$ with $-1 \leq \alpha_{i,j} \leq L - 1$. $\mathbf{Z}^0 = \mathbf{I}_L$ is the identity matrix of size $L \times L$. By convention, $\mathbf{Z}^{-1} = \mathbf{0}_L$ will denote the all-zero matrix of size $L \times L$. Integer $\alpha_{i,j}$ is referred to as the exponent of the associated circulant $\mathbf{h}_{i,j}$.

Associated with the PCM \mathbf{H} , we define the exponent matrix \mathbf{H}_b of size $m_b \times n_b$ as follows

$$\mathbf{H}_b = \begin{pmatrix} \alpha_{0,0} & \cdots & \alpha_{0,n_b-1} \\ \vdots & \ddots & \vdots \\ \alpha_{m_b-1,0} & \cdots & \alpha_{m_b-1,n_b-1} \end{pmatrix}.$$

The specification of \mathbf{H}_b , therefore, is sufficient to construct \mathbf{H} , and thus the code.

4.4.3.2 Basic LDPC Code Used in Construction

4.4.3.2.1 The parity check matrix for the LDPC codes shall be formed by using an $m_b \times n_b$ array of $L \times L$ square circulants, where m_b , n_b , and L are given in Table 4-2.

Table 4-2: LDPC Code Parameters

Code Rate	m_b	n_b	L
1/2	140	260	128
9/10	36	252	128

NOTE – The full $m_b L \times n_b L$ parity-check matrix \mathbf{H} can be constructed from the associated $m_b \times n_b$ exponent matrix \mathbf{H}_b with the following procedure:

```

for  $i = 0$  TO  $m_b - 1$ 
    for  $j = 0$  TO  $n_b - 1$ 
         $\mathbf{H}(iL:(i+1)L-1, jL:(j+1)L-1) = \mathbf{Z}^{\mathbf{H}_b(i,j)}$ 
    endfor
endfor
    
```

where $\mathbf{H}(i_1:i_2, j_1:j_2)$ is the submatrix spanning row indexes i_1 to i_2 and column indexes j_1 to j_2 within matrix \mathbf{H} .

4.4.3.2.2 The exponent matrix \mathbf{H}_b associated with the $m_b \times n_b$ array of circulants in 4.4.3.2.1 shall be reconstructed based on the masking matrix represented in annex C.

NOTES

- 1 In particular, Annex C has one table for the rate 1/2 code, and one for the 9/10 code. Each row of the masking matrix called table T has m_b lines. Each row in the table enumerates the position and exponent of all non-zero circulants in the corresponding row of \mathbf{H}_b . Accordingly, each row $i \in \{0, \dots, m_b - 1\}$ of the table contains at most n_b entries where each entry is a pair $(j, \alpha_{i,j})$ with $j \in \{0, \dots, n_b - 1\}$ being the column index in \mathbf{H}_b , and $\alpha_{i,j}$ being the exponent of the associated non-zero circulant $\mathbf{h}_{i,j}$.
- 2 The following procedure may be used to construct \mathbf{H}_b . First, each exponent matrix \mathbf{H}_b is initialized with ‘-1’ in each of the $m_b \times n_b$ entries. Let n_i denote the number of pairs $(j, \alpha_{i,j})$ in row i of the table. There are $2n_i$ total entries in row i of the table. Then the exponent matrix can be built as follows:

```

for  $i = 0$  TO  $m_b - 1$ 
    for  $j = 0$  TO  $n_i - 1$ 
    
```

$H_b(i, T(i, 2j)) = T(i, 2j + 1)$

endfor

endfor

- 3 The structure of the resulting parity-check matrix is as shown in Figure 4-2, where the size of the non-zero submatrices **A** to **F** for each supported code rate are listed in Table 4-3. Submatrices **E** and **F** are identity matrices. Submatrix **D** has the form of a bidiagonal matrix but with the second diagonal starting at line L . For ARA type, the **C** matrix, **F** matrix, and the all-zero matrix above **F** are not used.

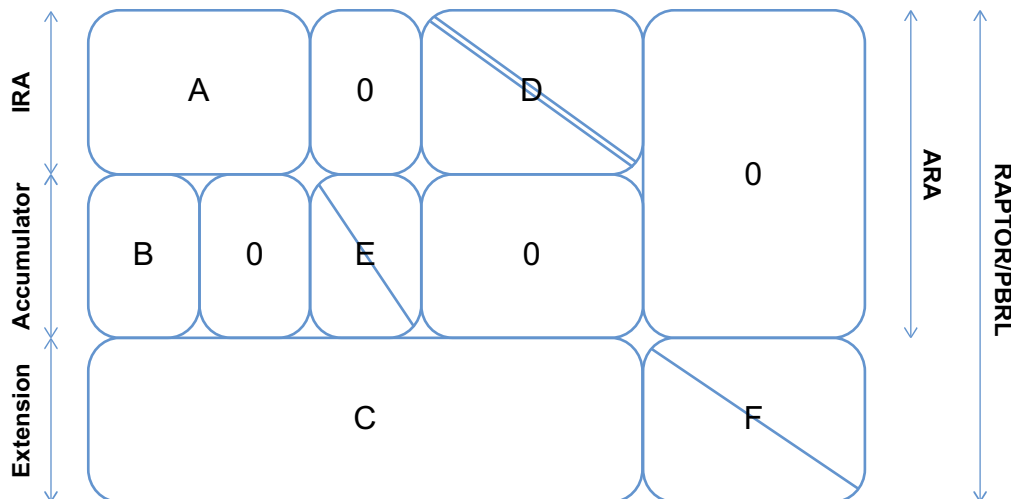


Figure 4-2: General Structure of the Parity-Check Matrix H

Table 4-3: Size of Each Submatrix within H

LDPC Code Rate	Size of matrix A	Size of matrix B	Size of matrix C	Size of matrix D	Size of matrix E	Size of matrix F
r	(m_A, n_A)	(m_B, n_B)	(m_C, n_C)	(m_D, n_D)	(m_E, n_E)	(m_F, n_F)
1/2	$40L \times 120L$	$20L \times 20L$	$80L \times 180L$	$40L \times 40L$	$20L \times 20L$	$80L \times 80L$
9/10	$24L \times 216L$	$12L \times 12L$	Not used	$24L \times 24L$	$12L \times 12L$	Not used

4.4.3.3 Encoding from H

4.4.3.3.1 The encoder shall accept as input a block of bits of length k as per Table 4-1.

4.4.3.3.2 Codewords consistent with the parity-check matrices in 4.4.3.2.1 shall be produced by the following algorithm.

With the sequence of codewords resulting from encoding of \mathbf{f} being denoted as $\mathbf{u} = \mathbf{u}^0, \mathbf{u}^1, \dots, \mathbf{u}^{C-1}$, with $\mathbf{u}^i = u_0, \dots, u_{n_b L-1}$, where $n_b L$ is the LDPC code length before puncturing, vector \mathbf{u}^i can be written as $\mathbf{u}^i = (\mathbf{f}^i, \mathbf{p}^i)$, with $\mathbf{p}^i = (\mathbf{p}_{acc}^i, \mathbf{p}_{ira}^i, \mathbf{p}_{ext}^i)$ being the vector of $m_b L$ parity-check bits to compute, where subvectors \mathbf{p}_{acc}^i , \mathbf{p}_{ira}^i , and \mathbf{p}_{ext}^i have length $n_E L$ bits, $n_D L$ bits, and $n_F L$ bits, respectively (see Table 4-3). The systematic encoding of \mathbf{f} by \mathbf{H} can be realized in four steps, as described below, with $\mathbf{f}^i = (f_0^i, \dots, f_k^i)$, k being the information block size defined in Table 4-1:

- 1 $\mathbf{p}_{acc}^i = \mathbf{f}_{acc}^i \times \mathbf{B}^t$ is computed, where vector $\mathbf{f}_{acc}^i = f_0^i, f_1^i, \dots, f_{n_B L-1}^i$ is formed of the first $n_B L$ bits of input frame \mathbf{f}^i ;
- 2 $\mathbf{p}_{ira}^i = \mathbf{q}^i \times (\mathbf{D}^t)^{-1}$ is computed, where $\mathbf{q}^i = \mathbf{f}^i \times \mathbf{A}^t$;
- 3 $\mathbf{p}_{ext}^i = (\mathbf{f}^i, \mathbf{p}_{acc}^i, \mathbf{p}_{ira}^i) \times \mathbf{C}^t$ is computed;
- 4 the codeword $\mathbf{u}^i = (\mathbf{f}^i, \mathbf{p}_{acc}^i, \mathbf{p}_{ira}^i, \mathbf{p}_{ext}^i)$ is formed.

NOTES

- 1 In step 1 above, n_B refers to the number of columns of matrix \mathbf{B} , and not the number of columns of the exponent matrix, which is n_b .
- 2 Because of the particular bidiagonal structure of \mathbf{D} , simple back-substitution can be used instead of explicitly calculating the inverse $(\mathbf{D}^t)^{-1}$.
- 3 The symbol \times denotes the matrix product, and superscript t denotes the transpose operation.

4.4.3.4 Puncturing

The LDPC codewords shall be reduced to length $n = 30720$ binary digits by puncturing the first P systematic bits, with P as given in Table 4-4.

Table 4-4: Number P of Punctured Bits

Code Rate	P
1/2	2560
9/10	1536

NOTE – LDPC encoding produces a systematic codeword of total length $n_b L$. As illustrated in Figure 4-3, the first P code bits in the LDPC codeword \mathbf{u}^i are discarded and not transmitted in order to form the codeword

$$\mathbf{q}^i = u_p^i, \dots, u_{n_b L-1}^i.$$

Each \mathbf{q}^i is called the LDPC codeword.

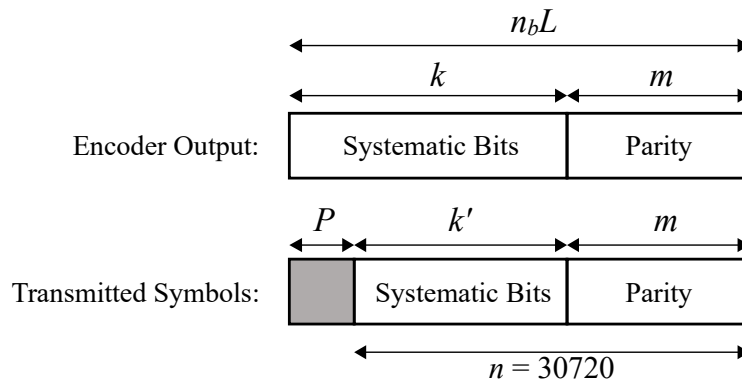


Figure 4-3: Format of the LDPC Codeword after Puncturing

4.5 CHANNEL INTERLEAVER

4.5.1 GENERAL

4.5.1.1 The sequence of RS codeblocks or LDPC codewords $\mathbf{q} = \mathbf{q}^0, \mathbf{q}^1, \dots, \mathbf{q}^{C-1}$ shall be channel-interleaved with a block interleaver as shown in figure 4-4 and described herein.

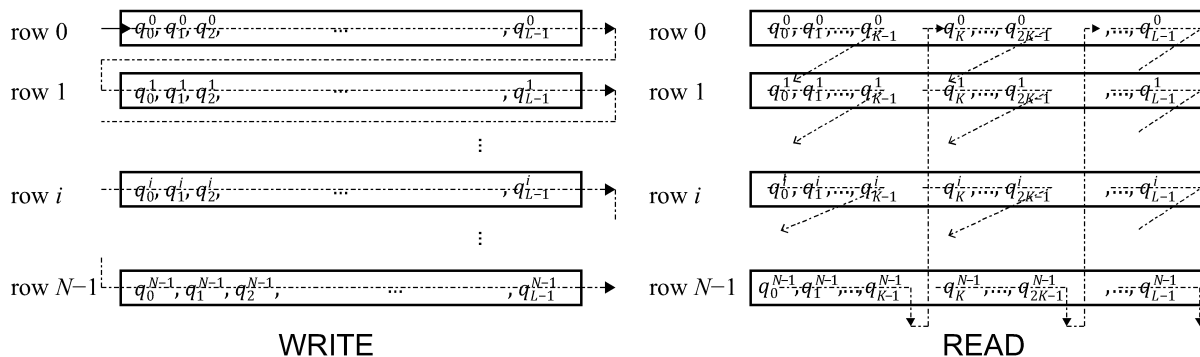


Figure 4-4: Block Channel Interleaver

NOTE – Each codeblock or codeword \mathbf{q}^i , composed of L bits $\mathbf{q}^i = q_0^i, q_1^i, \dots, q_{L-1}^i$, is written row-wise in the interleaver, and the output is obtained by reading the interleaver column-wise by blocks of length K .

4.5.1.2 The interleaver shall be parameterized with three parameters: K , the number of binary digits per interleaver symbol; N , the number of rows in the channel interleaver; and L , the number of columns in the interleaver.

4.5.1.3 The values of K , L , and N shall satisfy the constraints in Table 4-5.

Table 4-5: Constraints on Block Interleaver Parameters

Code	K	L	N
RS	Multiple of 8; factor of L	$2040 \times l$	At most $2^{23} - 1$
LDPC	{64, 128, 256, 512, 1024}	30720	At most 2^{18}

NOTES

- 1 L here is not related to the L used in the definition of the LDPC encoder.
- 2 The parameter K allows more efficient access to memory, compared to a bitwise block interleaver. This can be important for high-throughput data transfer.
- 3 N is an integer. The maximum allowable value of N is a compromise between memory hardware constraints onboard and on-ground, and the maximum interleaving time. The maximum N is lower for LDPC than for RS because soft decision decoding at the receiving end requires quantization of the symbols, which takes more memory than the hard-decisions the RS decoder uses.
- 4 N can be chosen in accordance with the repeat factors SF (see 4.6) and q_d (4.9.1) in order to have an interleaving time which is similar for all the selected repeat factors during a mission phase

4.5.2 NUMBER OF ROWS IN THE INTERLEAVER

When RS coding is used, the number of rows in the interleaver, N , shall equal either N_N or N_N/q_d , where N_N is the nominal number of interleaver rows and it is a managed parameter. When LDPC coding is used, N shall be signaled by in-band signaling as specified in 4.8.2.6.4.7.

4.5.3 CHANNEL INTERLEAVER INPUT NOTATION

The sequence of RS codeblocks or LDPC codewords $\mathbf{q} = \mathbf{q}^0, \mathbf{q}^1, \dots, \mathbf{q}^{C-1}$ shall be a vector of vectors that can be viewed as a single vector of interleaver binary digits,

$$\hat{\mathbf{q}} = q_0, q_1, \dots, q_{L \times C - 1},$$

where for $i \in \{0, 1, \dots, C - 1\}$ and $j \in \{0, 1, \dots, L - 1\}$, $\hat{q}_{L \times i + j} = q_j^i$.

4.5.4 CHANNEL INTERLEAVER OPERATION

4.5.4.1 Write Order and Read Order

The input interleaver binary digits \hat{q} shall be serially written into the interleaver row-wise, and then, the interleaver symbols of K binary digits are serially read out column-wise.

4.5.4.2 Input and Output Relationship

The correspondence between r_j^i , the j^{th} bit of the i^{th} interleaver output frame, and q_k^l the k^{th} bit of the l^{th} interleaver input frame shall be:

$$r_j^i = q_k^l \text{ with } i = \left\lfloor \frac{l}{N} \right\rfloor, \text{ and } j = \left\lfloor \frac{k}{K} \right\rfloor K N + K \left(l - \left\lfloor \frac{l}{N} \right\rfloor N \right) + k,$$

where $\lfloor x \rfloor$ denotes the integer part of x

4.5.4.3 Reindexing Channel Interleaver Output to Form Interleaved Codewords

The sequence \hat{r} may be re-indexed into $R = C/N$ blocks each containing $L \times N$ binary digits:

$$\mathbf{r}^0, \mathbf{r}^1, \dots, \mathbf{r}^{R-1},$$

where for $i \in \{0, 1, \dots, R - 1\}$, the i^{th} block is denoted $\mathbf{r}^i = r_0^i, r_1^i, \dots, r_{L \times N - 1}^i$, and for $j \in \{0, 1, \dots, L \times N - 1\}$,

$$r_j^i = \hat{r}_{L \times Ni + j}.$$

NOTE – Each \mathbf{r}^i is called an interleaved block.

4.5.4.4 Transmission Closure

The Data Link Layer shall ensure that the cumulative size of the transfer frames to be transmitted is compatible with the size of the interleaver.

4.6 REPEAT

4.6.1 When RS coding is used, no repeat shall be applied at this stage (equivalently, $SF = 1$).

NOTE - SF refers to ‘spreading factor’. The repetition, combined with the mandatory pseudo-randomizer (4.7), is a spread spectrum technique. The bit repetition contributes to the Variable Data Rate method as described in 4.8.2.6.4.2.

4.6.2 When LDPC coding is used, each interleaved block binary digit shall be repeated so that it appears SF times, $SF \in \{1, 2, 4, 8, 16\}$, as follows:

- a) for $j \in \{0, 1, \dots, SF(R \times L \times N) - 1\}$, the j^{th} symbol at the output of the repeater shall be

$$\hat{\alpha}_j = \hat{r}_{\lfloor j/SF \rfloor},$$

where $\lfloor x \rfloor$ denotes the integer part of x ;

- b) the sequence α shall be composed of R blocks each containing $SF \times L \times N$ binary digits:

$$\alpha = \alpha^0, \alpha^1, \dots, \alpha^{R-1},$$

where for $i \in \{0, 1, \dots, R - 1\}$, the i^{th} block is denoted $\alpha^i = \alpha_0^i, \alpha_1^i, \dots, \alpha_{SF \times L \times N - 1}^i$, and for $j \in \{0, 1, \dots, SF \times L \times N - 1\}$,

$$\alpha_j^i = \hat{\alpha}_{SF \times L \times N \times i + j}.$$

NOTES

- 1 Each α^i is called a repeated interleaved block.
- 2 When LDPC coding is used, the value of SF is handled by inline communications protocol.

4.7 PSEUDO-RANDOMIZER

4.7.1 RANDOMIZATION OF REED-SOLOMON CODEBLOCKS

4.7.1.1 Each interleaved RS codeblock shall be pseudo-randomized by performing the digit-wise modulo-2 addition with a pseudo-random sequence, as shown in Figure 3-4 described in 3.5 and herein.

4.7.1.2 For $i \in \{0, 1, \dots, R - 1\}$, the i^{th} pseudo-randomized interleaved block is denoted $\beta^i = \beta_0^i, \beta_1^i, \dots, \beta_{SF \times L \times N - 1}^i$, where for $j \in \{0, 1, \dots, SF \times L \times N - 1\}$, the j^{th} symbol of the i^{th} block is

$$\beta_j^i = \alpha_j^i \oplus p_j,$$

where \oplus represents modulo-2 addition and p_j is defined in 3.5.2.

NOTE – Each β^i is called an RS major code frame.

4.7.2 RANDOMIZATION OF REPEATED INTERLEAVED BLOCKS OF LDPC CODEWORDS

4.7.2.1 Description

Each interleaved repeated block of LDPC codewords shall be pseudo-randomized by performing the digit-wise modulo-2 addition with a pseudo-random sequence, as shown in Figure 4-5 and described herein.

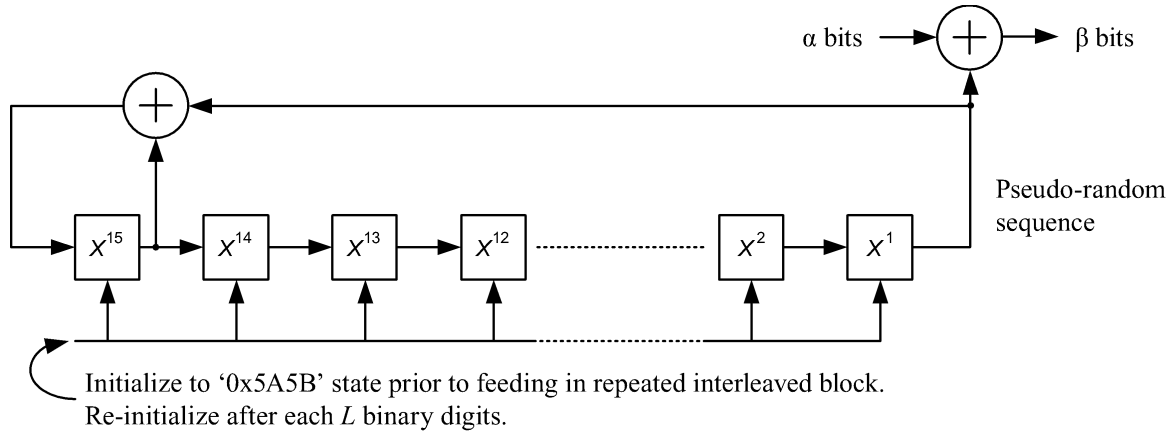


Figure 4-5: Pseudo-Randomizer

NOTES

- 1 For $i \in \{0, 1, \dots, R - 1\}$, the i^{th} pseudo-randomized interleaved block is denoted $\boldsymbol{\beta}^i = \beta_0^i, \beta_1^i, \dots, \beta_{SF \times L \times N - 1}^i$, where for $j \in \{0, 1, \dots, SF \times L \times N - 1\}$, the j^{th} symbol of the i^{th} block is

$$\beta_j^i = \alpha_j^i \oplus \gamma_{j \bmod L},$$

where \oplus represents modulo-2 addition, and γ_j is defined in 4.7.2.2.

- 2 As mentioned before, $L = 30720$ binary digits.
- 3 Each $\boldsymbol{\beta}^i$ is called an LDPC major code frame.
- 4 The pseudo-randomizer defined in this section ensures sufficient randomness and sufficient bit transition density to allow proper synchronization of the decoder.

4.7.2.2 Sequence Specification

The pseudo-random sequence $\gamma_0, \gamma_1, \dots, \gamma_{30720-1}$ shall correspond to the first 30720 bits of a truncated PRBS15 sequence generated by the monic polynomial:

$$g(D) = D^{15} + D^{14} + 1.$$

NOTE – The output bit of the sequence is the Least Significant Bit (LSB) of the linear feedback shift register. In Figure 4-5, the LSB of the LFSR is denoted x^1 .

4.7.2.3 Sequence Initialization

4.7.2.4 This sequence shall begin at the first digit of the repeat interleaved block and the sequence generator shall be reset after each L binary digits. The sequence is continuously repeated until the end of the repeat interleaved block is reached, as illustrated in Figure 4-7.

4.7.2.5 The sequence generator shall be initialized with initialization pattern = 0x5A5B.

Index	X^{15}	X^{14}	X^{13}	X^{12}	X^{11}	X^{10}	X^9	X^8	X^7	X^6	X^5	X^4	X^3	X^2	X^1
Value	1	0	1	1	0	1	0	0	1	0	1	1	0	1	1
0x	5			A				5			B				

Figure 4-6: Pseudo-Randomizer LFSR Initialization

NOTE – The first output bits after initialization, using transmission convention (first is MSB) are: 1101 1010 0101 1010 1101 1000 1101 1001 0010 0001 0010 0011.

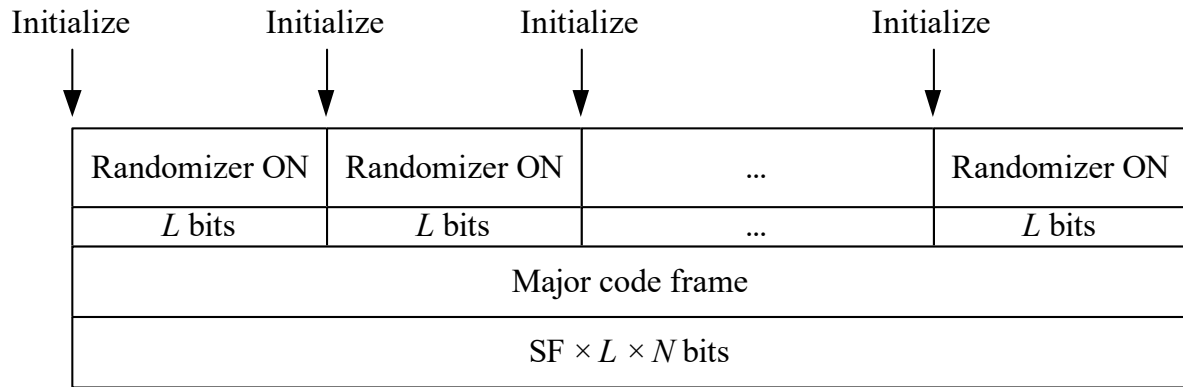


Figure 4-7: Pseudo-Randomizer Initializations Each L Bits

4.8 SYNC LAYER FRAMING

4.8.1 SYNC LAYER FRAMING FOR REED-SOLOMON

4.8.1.1 Sync Layer Subframes

Each RS major code frame of $2040 \times I \times N$ binary digits shall be split into N_{SF} subframes, each of length $2040 \times I \times N_L$ binary digits, where N is a multiple of N_L .

NOTES

- 1 N_L is a managed parameter.
- 2 N_{SF} is computed as $N_{SF} = N/N_L$, where N is the number of rows of the channel interleaver.

4.8.1.2 Sync Layer Framing with No Subframes ($N_L = N$, i.e., $N_{SF} = 1$)

4.8.1.2.1 General

When $N_L = N$, that is, when the major code frame is not split into multiple subframes ($N_{SF} = 1$), then the sync layer frame shall comprise an attached sync layer frame marker as defined in 4.8.1.2.2, followed by the major code frame.

4.8.1.2.2 Sync Layer Frame Marker

The Sync Layer Frame Marker (SLFM) shall comprise a Frame Synchronization Marker (FSM), which corresponds to the ASM as defined in 3.3.2.

4.8.1.3 Sync Layer Framing with Subframes ($N_L < N$, i.e., $N_{SF} > 1$)

4.8.1.3.1 General

When $N_L < N$, that is, when the major code frame is divided into subframes ($N_{SF} > 1$), the sync layer frame shall comprise an SLFM, followed by subframe 0, followed by a sequence of Secondary SLFM (SSLFM) and subframe pairs, totaling N_{SF} subframes, as shown in Figure 4-8: Sync Layer Frame for O3K-RS.

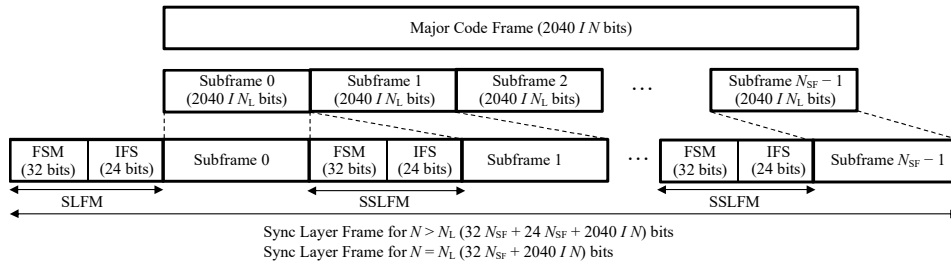


Figure 4-8: Sync Layer Frame for O3K-RS

4.8.1.3.2 Sync Layer Frame Marker

When $N_{SF} > 1$, the SLFM shall comprise an FSM, which corresponds to the ASM as defined in Sequence specification, followed by an optional interleaver frame signaling (IFS) field, composed of 24 bits: 23-bit counter set to zero (23 zeroes) and one-bit parity set to zero.

NOTE – The presence or absence of the counter is indicated by a managed parameter.

4.8.1.3.3 Secondary Sync Layer Frame Marker

When $N_{SF} > 1$, the SSLFM shall comprise an FSM, which corresponds to the ASM as defined in Sequence specification, followed by an optional IFS field composed of 23-bit counter and

1-bit counter parity. The value of the counter shall be i in the SSLFM immediately preceding subframe i .

4.8.2 SYNC LAYER FRAMING FOR LOW-DENSITY PARITY-CHECK

4.8.2.1 Sync Layer Subframes

Each major code frame of $SF \cdot 30720N$ binary digits shall be split into N_{SF} subframes, each of length $30720 N_L$ binary digits, where $SF \cdot N$ is a multiple of N_L .

NOTES

- 1 N_L is a managed parameter.
- 2 N_{SF} is computed as $N_{SF} = SF \cdot N / N_L$, where N is the number of rows of the channel interleaver, and SF the repeat factor described in 4.6.

4.8.2.2 Sync Layer Framing with No Subframes ($N_L = SF \cdot N$, i.e., $N_{SF} = 1$)

When $N_{SF} = 1$, that is, when the major code frame is not split into multiple subframes, then the sync layer frame shall comprise SLFM as defined in Sync Layer Frame Marker, followed by the major code frame, as shown in Figure 4-9.

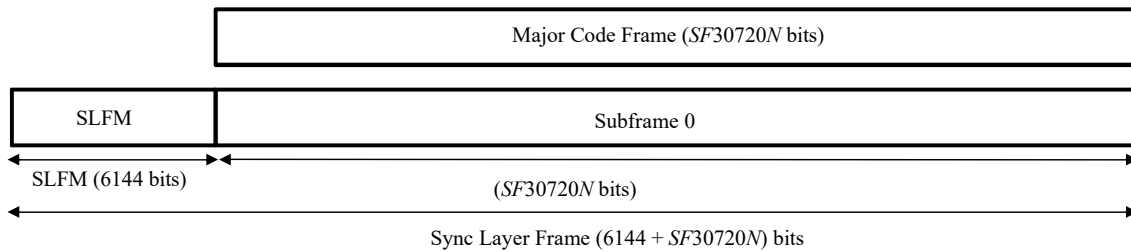


Figure 4-9: Sync Layer Frame for O3K LDPC with No Subframes

NOTE – In this situation, there are no SSLFMs in the sync layer frame.

4.8.2.3 Sync Layer Framing with Subframes ($N_L < SF \cdot N$, i.e., $N_{SF} > 1$)

4.8.2.3.1 General

When $N_{SF} > 1$, the sync layer frame shall comprise a SLFM, followed by subframe 0, followed by a sequence of SSLFM and subframe pairs, totaling N_{SF} subframes, as shown in Figure 4-10.

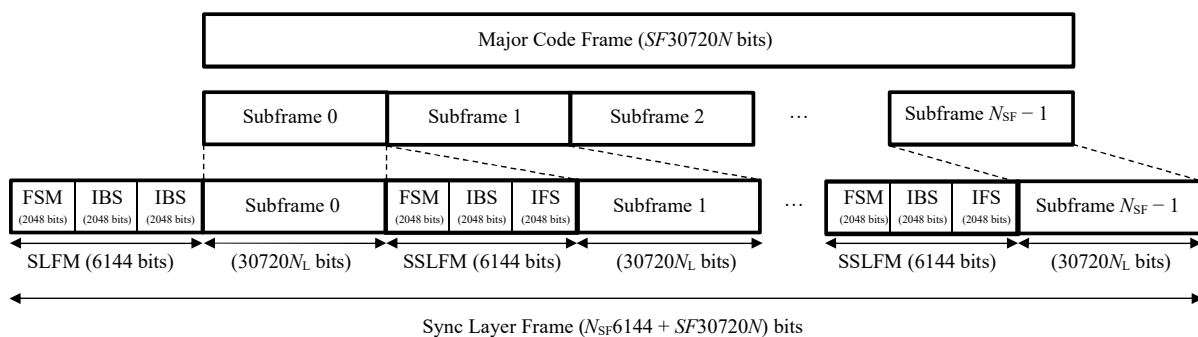


Figure 4-10: Sync Layer Frame for O3K-LDPC with Multiple Subframes

4.8.2.3.2 Sync Layer Frame Marker

4.8.2.3.2.1 General

The SLFM shall comprise an FSM, a first In-Band Signaling (IBS) field, and a second IBS field, as described in the subsections below.

4.8.2.3.2.2 Frame Synchronization Marker

The FSM shall be the 2048-bit sequence defined in ANNEX D, using the initial condition $A = 2$.

NOTES

- 1 The sequence represented in hexadecimal notation is:

```
0xC0173D2255032836E2ACAA887EF8668EA64A6911A589AE2E498EC979215A
54575C3D8D71AC679AFBED06D9DAB4B6084B80EC4B3EB7A1A94BD976EE6
7C5612D41813096A8DB50A22B67514E0F90E78F36626E79971105791522CF17C
83374CF7EAD26C7CBA86750F130D1A4D5405141BB35C04BB25EA4F91FE06A
644956088E35E7EB4D510D5842651CFB5CF4039BD2FD4641FF9B47E1453FC1
C7861E510E56BA12A6B5D94CE69346846AFFC4375FB8D4B252F97895A170D2
CDF92F52A6A9F8F1A9ADDCF3C45CB041B3F2FF9A4FEB08DA4DFC793DD14
9E59AC61A76421D7B8FA210071EE4E15F1E199692AE80B47E933E42CDAC123
F5EFB4561B4D1569CE7184,
```

with the left-side bit being the MSB of the FSM to be sent first.

- 2 This FSM may appear long compared to the traditional CCSDS synchronization marker described in [7]. This length is justified by the need to have a robust start-of-frame synchronization when the repeat factor SF (4.6) is larger than one, as in the example of Table 4-7.

4.8.2.3.2.3 First In-Band Signaling Field

The first IBS field shall be the sequence defined in 4.8.2.6.5.

4.8.2.3.2.4 Second In-Band Signaling Field

The second IBS field shall be identical to the first IBS field.

NOTE – This repeated IBS field indicates the start of a major code frame.

4.8.2.3.3 Secondary Sync Layer Frame Marker

The SSLFM shall be identical to the SLFM defined in 4.8.2.3.2, except that the second in-band IBS field shall be replaced by an IFS field, defined as the 2048-bit sequence defined in annex D, using the initial condition $A = 6$.

NOTES

- 1 This IFS field indicates all the subframes of a major code frame that are not the first subframe.
- 2 The SSLFM includes no counter to indicate the position of the subframes inside the block interleaver, as in the RS coding case. It is up to the receiver to detect the beginning of the sync layer frame, signaled by two IBS fields.

4.8.2.4 IDLE Sync Layer Subframes

4.8.2.4.1 General

IDLE Sync Layer (ISL) Subframes (ISLFs) may be generated when needed by the coding and synchronization sublayer to maintain synchronization process at sync layer frame level. When used, they shall have the structure defined in the following subsections and shown in Figure 4-11.

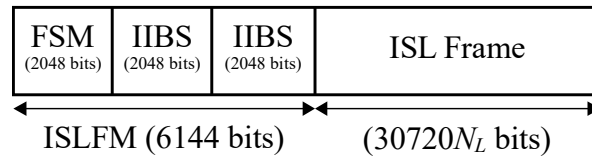


Figure 4-11: IDLE Sync Layer Subframe

4.8.2.4.2 Frame Synchronization Marker

4.8.2.4.2.1 General

The FSM shall be the 2048-bit sequence defined in ANNEX D, using the initial condition $A = 2$.

4.8.2.4.2.2 First IDLE In-Band Signaling Field

The first IDLE In-Band Signaling (IIBS) field shall be the 2048-bit sequence defined in ANNEX D, using the initial condition $A = 4$.

4.8.2.4.2.3 Second IDLE In-Band Signaling Field

The second IIBS field shall be identical to the first IIBS field.

4.8.2.4.2.4 IDLE Sync Layer Subframe Payload

The ISLF payload shall be a vector computed using the pseudo-randomizer defined in 4.7.2. The vector corresponds to the output of the pseudo-randomizer (β bits) when the all ‘zeroes’ vector of length $30720N_L$ is provided at the input of the randomizer (α bits).

4.8.2.5 Discussion

The resulting sync layer frame $\mathbf{y} = \mathbf{y}^0, \mathbf{y}^1, \dots, \mathbf{y}^{(R+I)-1}$ comprises a combination of $\hat{\mathbf{y}} = \hat{\mathbf{y}}^0, \hat{\mathbf{y}}^1, \dots, \hat{\mathbf{y}}^{R-1}$ sync layer data frames and $\widehat{\mathbf{y}}_{idle} = \widehat{\mathbf{y}}_{idle}^0, \widehat{\mathbf{y}}_{idle}^1, \dots, \widehat{\mathbf{y}}_{idle}^{I-1}$ ISLF, ordered in a monotonic way, but where the occurrence of $\hat{\mathbf{y}}^i$ and $\widehat{\mathbf{y}}_{idle}^i$ cannot be known in advance.

An illustration of ISLF insertion is provided by Figure 4-12, in which ISLFs are inserted between two consecutive major code frames.

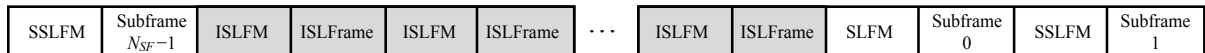


Figure 4-12: IDLE Sync Layer Subframes Inserted between Two Major Code Frames

The mechanism of ISLFs can be used to ease ground receiver synchronization during acquisition or reacquisition of the link. At the receiver end, ISLFs are discarded.

4.8.2.6 In-Band Signaling

4.8.2.6.1 Overview

In-band signaling is a method to communicate dynamically changing signal parameter values to the receiving end, using the communications link itself. The in-band signaling can be detected at the receiving end, which allows the receiver to recover the parameter values and properly configure itself to receive the remainder of the transmission.

The in-band signaling is accomplished with the IBS. The IBS comprises 2048 binary digits that contain the Mode ID as defined in 4.8.2.6.4, which identifies the emitter configuration table. The IBS is generated using the Gold codes defined in ANNEX D. The set of allowable transmission modes is cataloged in an emitter configuration table known to both the sender and receiver.

In-band signaling is only supported by O3K LDPC coding, where dynamic links are expected. It is not used with HPE. Where dynamic HPE links exist, dynamically changing transmission parameter values could be supported by requiring the receiving end to perform blind acquisition of the changing values, for example, a shift in the PPM order or repeat factor. This is possible within the framework of the HPE standard without utilizing in-band signaling. It is not used by O3K RS either. Where dynamic O3K RS links exist, dynamically changing the symbol rate is supported by requiring the receiving end to perform blind acquisition of the changing value q_d , the repeat factor defined in 4.9.1.

4.8.2.6.2 Use of In-Band Signaling

In-band signaling shall be used when LDPC coding is used. It shall not be used when RS coding is used.

4.8.2.6.3 Signaled Parameters

When in-band signaling is used, it shall be used to indicate the code rate (r), the repetition factor (SF), the number of rows of the channel interleaver (N), and the size of the channel interleaver block (K).

4.8.2.6.4 Emitter Configuration Table

4.8.2.6.4.1 General

Each distinct parameter set (r , SF , N , K) used in transmission shall be represented in a row of the emitter configuration table.

NOTES

- 1 The emitter configuration is defined as a set of parameters that can dynamically change which values are to be sent to the receiver during a mission phase.
- 2 The table is a managed parameter known to the transmitting and receiving ends of the link and held fixed during each mission phase.

4.8.2.6.4.2 Mode ID

Each row of the emitter configuration table shall have a unique numeric Mode ID associated with transmission parameter values (r, SF, N, K) .

4.8.2.6.4.3 Description

Each row of the emitter configuration table shall have a textual description of the mode.

4.8.2.6.4.4 Code Rate

Each row of the emitter configuration table shall include a code rate $r \in \{1/2, 9/10\}$.

4.8.2.6.4.5 Repetition Factor

Each row of the emitter configuration table shall include a repetition factor $SF \in \{1, 2, 4, 8, 16\}$.

4.8.2.6.4.6 Number of Rows in Channel Interleaver

Each row of the emitter configuration table shall include a number of rows of the channel interleaver N , an integer ranging inclusively from 1 to 2^{18} .

4.8.2.6.4.7 Size of the Channel Interleaver Block

Each row of the emitter configuration table shall include a size of the channel interleaver block $K \in \{64, 128, 256, 512, 1024\}$.

NOTES

- 1 The in-band signaling is compatible with a static data rate used during the whole mission, a constant data rate used for a specific overflight or mission phase, a dynamic data rate that is predetermined (preprogrammed) based on elevation profile, and a dynamic data rate that is modified in a near real-time fashion to react to observed channel conditions in order to maximize data return.

- 2 The Mode ID identifies the current configuration of the emitter. If the number of emitter configuration modes defined for a mission phase is high, the IBS detection algorithm may be less robust. During a pass, having only two code rates and 5 different repeat factors, it is not relevant to define more than 6 different configurations.
- 3 Example emitter configuration tables are given in Table 4-6, for the case of a constant data rate, and in Table 4-7 for a dynamic data rate scenario.

Table 4-6: Example 1 of Emitter Configuration Mode Table for O3K Optical Communications Using LDPC Coding

Mode ID	Description	Code rate r	Repeat factor SF	Channel interleaver block K	Number of rows in the interleaver N
0	PL Frame	1/2	1	128	262144

Table 4-7: Example 2 of Emitter Configuration Mode Table for O3K Optical Communications Using LDPC Coding

Mode ID	Description	Code rate r	Repeat factor SF	Channel interleaver block K	Number of rows in the interleaver N
0	PL Frame2_SF16	1/2	16	128	8192 (65536/8)
1	PL Frame2_SF8	1/2	8	128	16384 (65536/4)
2	PL Frame2_SF4	1/2	4	128	16384 (65536/4)
3	PL Frame2_SF2	1/2	2	128	65536
4	PL Frame2_SF1	1/2	1	128	65536
61	PL Frame9_SF1	9/10	1	128	65536

4.8.2.6.5 In-Band Signaling Sequence Specification

When Mode ID m is used, $0 \leq m \leq 61$, the IBS field shall be the 2048-bit sequences defined in ANNEX D, using the initial condition $A = 2(m + 4)$.

NOTE – For example, with the emitter configuration table of Table 4-7, the initial conditions A to generate the Gold sequences, which identify the configuration, are given in the Table 4-8.

Table 4-8: Example of Initial Condition A

Mode ID	0	1	2	3	4	61
Initial condition A	8	10	12	14	16	130

4.9 REPEAT

4.9.1 REPEAT FOR RS

4.9.1.1 When RS coding is used, each coded OOK symbol shall be repeated so that it appears q_d times, where q_d is the repeat factor.

4.9.1.2 For $j \in \{0, 1, \dots, \text{Sync_Layer_Frame_length} - 1\}$, the j^{th} symbol at the output of the repeater shall be

$$t_j = y_{\lfloor j/q_d \rfloor},$$

where $\lfloor x \rfloor$ denotes the integer part of x .

NOTES

- 1 The minimum slot width of the mission phase is given by the managed parameter T , the telemetry signaling slot width in nanoseconds (reference [3]). The repeat factor q_d will make it effectively longer by a factor of 2^w , with $0 \leq w \leq (13 - \log_2(T \times 10 \times 10^9))$. After repetition, the resulting effective slot width is one of the slot widths defined in reference [3], subsection 5.7.
- 2 The repeat factor used by the transmitter is recovered by the receiver itself, and therefore no in-band signaling or management is required.
- 3 The subset of allowable repeat factors that a mission phase uses is known at the receiving end. This enables the receiver to search over a smaller subset instead of the entire set of allowable repeat factors. This subset is a managed parameter, the Repeat Factor list, defined in Table 6-2.
- 4 The interleaver length can be affected by the repeat factors. The number of rows of the interleaver, N , is defined in the managed parameter and detailed in 4.5.1.2. Its value can either be kept constant, $N = N_N$, or it can be scaled with respect to the repeat factors, that is, $N = N_N/q_d$.
- 5 For example, if the maximum data rate selected in the Physical Layer is 10 Gb/s and repeat factors list in the managed parameter q_d is $\{1, 4, 8\}$: this means that the maximum data rate of 10 Gb/s is transmitted and data rate variation scheme can be used to lower the data rate to 2.5 Gb/s (using $q_d = 4$) and 1.25 Gb/s (using $q_d = 8$).

4.9.2 REPEAT FOR LDPC

When LDPC coding is used, q_d shall be equal to 1, which means that no repetition is applied.

4.10 OOK MODULATION MAPPING

OOK modulation mapping shall be performed by directly mapping the coded binary digits to Non-Return-to-Zero (NRZ)-OOK symbols: coded binary digit 0 gives an NRZ-OOK symbol 0, and coded binary digit 1 gives an NRZ-OOK symbol 1.

4.11 TRANSFER FRAME VALIDATION

4.11.1 OVERVIEW

4.11.1.1 O3K RS

The receiving end of the telemetry transmission detects the symbol rate, obtains timing synchronization, and uses the FSMs to obtain frame synchronization. After removal of the FSMs, the major code frames are recovered, derandomized, and deinterleaved, and the RS codewords are decoded.

4.11.1.2 O3K LDPC

The receiving end of the telemetry transmission obtains a sync layer frame, then uses the FSMs to obtain frame synchronization. The IBS of the SLFMs indicates the emitter configuration mode. The IFS of the SSLFMs indicates the presence of a sync layer subframe. The IIBS of the IDLE Sync Layer Subframe Markers (ISLFMs) indicates the presence of an ISLF.

After removal of the sync layer frame markers, SLFM and SSLFM, of the frames containing data, the major code frames are recovered, derandomized, despread, and deinterleaved, and the LDPC codewords are decoded. If frame adaptation is performed, the resulting sequence of SMTFs is synchronized by identifying the ASMs, which are removed, yielding the transfer frames.

ISLFs are discarded by the receiver.

4.11.2 QUALITY INDICATOR

A transfer frame shall be marked valid if it is recovered from one or more correctly decoded codewords (RS or LDPC); a transfer frame shall be marked invalid if it is recovered from one or more incorrectly decoded codewords.

4.11.3 OPTIONAL FRAME ERROR CONTROL FIELD IN TRANSFER FRAME

The FECF defined in reference [1], [2], or [8] is optional, and the system designer may choose to use it for additional frame validation in the Data Link Protocol Sublayer.

4.12 SEQUENCE INDICATOR

A Sequence Indicator shall be 'zero' when a transfer frame is the direct successor of the previous one, and 'one' when a gap has been detected.

5 HPE BEACON AND OPTIONAL ACCOMPANYING DATA TRANSMISSION SIGNALING

5.1 OVERVIEW

The telemetry and beacon signals operate simultaneously and in opposite directions on the link. The beacon signal may or may not include data transmission. When the beacon signal does not carry data, it is a simple square wave, as described in reference [3]. When the beacon signal carries data, the Coding and Synchronization Sublayer takes AOS or USLP fixed-length transfer frames (reference [2] or [8]) as input and produces a binary vector indicating the positions of pulsed slots at the output. The functional blocks of the architecture are shown in Table 5-1, along with the notation used in the following sections that defines these functions mathematically. It should be understood that the functions need not be implemented explicitly as defined here; any implementation producing the proper pattern of pulsed slots complies with the standard.

NOTES

- 1 The specification given here is intended to provide a beacon and optional low-data-rate transmission. This specification is compatible with higher data rates, by splitting each signal slot into narrower slots, which may be independently modulated at a higher rate.
- 2 In RF communications, the operations of a spacecraft receiver are such that it is convenient to require uplink transmissions to consist, first, of an unmodulated carrier, and then a carrier modulated with an acquisition sequence, prior to sending data. For this reason, the TC synchronization and coding standard (reference [6]) specifies a Physical Layer Operations Procedure (PLOP) that details the manner and order in which these types of signals are to be transmitted, in order to establish a physical connection between the transmitter and receiver.

By contrast, in optical communications, the spacecraft receiver does not require transmission of an unmodulated carrier or an acquisition sequence. For this reason, this Recommended Standard does not specify a PLOP, and in this respect, the operation of the AOS/USLP transfer frame signaling is similar to the telemetry signaling.

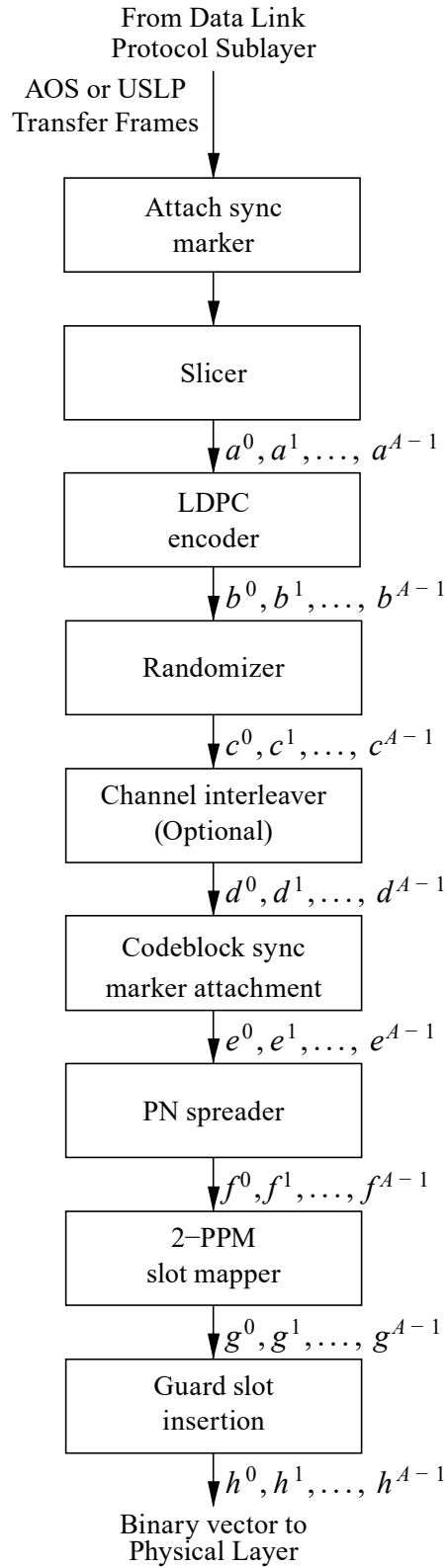


Figure 5-1: Functional Diagram for AOS/USLP Transfer Frame Signaling

5.2 INPUT TO CODING AND SYNCHRONIZATION SUBLAYER

5.2.1 AOS/USLP TRANSFER FRAME

The input to the Coding and Synchronization Sublayer shall be an AOS or USLP transfer frame. The structures of the AOS and USLP transfer frames are defined in references [2] and [8], respectively.

5.2.2 AOS/USLP TRANSFER FRAME LENGTH

The length of the AOS or USLP transfer frames shall be a fixed length, controlled by a managed parameter.

5.3 ATTACHED SYNCHRONIZATION MARKER

ASMs shall be inserted before each AOS/USLP transfer frame, in the manner described in 4.3, to form SMTFs.

NOTE – The ASM together with the AOS/USLP transfer frame is called an SMTF. The SMTFs have a fixed length, because the AOS/USLP transfer frame has a fixed length, controlled by a managed parameter.

5.4 SLICER

5.4.1 SLICER LENGTH

The SMTFs shall be sliced into blocks of length $k \in \{64, 256, 1024\}$, using the method specified in 4.3.4, where k is a managed parameter.

NOTE – The sliced stream of data forms a contiguous sequence of A binary slices (vectors), $\mathbf{a}^0, \mathbf{a}^1, \dots, \mathbf{a}^{A-1}$. Each slice is k binary digits in length. For $i \in \{0, 1, \dots, A-1\}$, the i^{th} slice is denoted

$$\mathbf{a}^i = a_0^i, a_1^i, \dots, a_{k-1}^i,$$

where $a_j^i \in \{0, 1\}$ is the j^{th} bit of the i^{th} slice.

5.4.2 ZERO FILL

At transmission closure, the slicer output shall be zero-filled with the minimum number of ‘zeroes’ so that its length is a multiple of k .

5.5 LDPC ENCODER

Each information block a^i shall be encoded using the (128, 64) or (512, 256) binary LDPC code defined in section 4 of reference [6] or the (2048, 1024) LDPC code defined in section 7 of reference [7].

NOTE – For $i \in \{0, 1, \dots, A-1\}$, the $n = 2k$ binary symbols of the i^{th} LDPC-encoded transfer frame are denoted

$$\mathbf{b}^i = b_0^i, b_1^i, \dots, b_{n-1}^i,$$

where $b_j^i \in \{0, 1\}$ is the j^{th} binary digit of the i^{th} codeword.

5.6 PSEUDO-RANDOMIZER

5.6.1 APPLICATION OF PSEUDO-RANDOMIZER

Each codeword \mathbf{b}^i shall be pseudo-randomized by performing the digit-wise modulo-2 addition with a binary pseudo-random sequence p_0, p_1, p_2, \dots as follows: for $i \in \{0, 1, \dots, A-1\}$, the i^{th} pseudo-randomized codeword is $\mathbf{c}^i = c_0, c_1, \dots, c_{n-1}$ and for $j \in \{0, 1, \dots, n-1\}$, the j^{th} binary symbol is given by

$$c_j^i = b_j^i \oplus p_j,$$

where \oplus represents modulo-2 addition.

5.6.2 SEQUENCE SPECIFICATION

The pseudo-random sequence shall be as defined in 3.5.2.

5.6.3 SEQUENCE INITIALIZATION

The shift register shall be initialized to the ‘all ones’ state at the beginning of each codeword.

5.7 CHANNEL INTERLEAVER

5.7.1 A channel interleaver may be used with AOS transfer frame signaling.

NOTE – Usage is controlled by a managed parameter.

5.7.2 If a channel interleaver is used, it shall operate on pseudo-randomized codewords and be as described in 4.5.

5.7.3 For the purposes of the channel interleaver, the parameter S in 4.5 should be understood to refer to the number of symbols in each AOS transfer frame pseudo-randomized codeword, $S = n = 2k$.

5.7.4 The number of rows in the AOS transfer frame channel interleaver may be denoted N_u , and the shift register increment in the AOS transfer frame channel interleaver may be denoted B_u .

NOTE – The output of the channel interleaver is $\mathbf{d}^0, \dots, \mathbf{d}^{A-1}$.

5.8 CODEWORD SYNCHRONIZATION MARKER

5.8.1 When the (128, 64) or (512, 256) LDPC code is used, a CSM of 16 binary symbols shall be prepended to each pseudo-randomized (and optionally interleaved) codeword.

5.8.2 The CSM shall be EB90, in hexadecimal, which is the same marker defined in subsection 5.2.2 of reference [6].

5.8.3 When the (2048, 2014) LDPC code is used, a CSM of 64 binary symbols shall be prepended. The CSM in that case shall be 034776C7272895B0, in hexadecimal, which is the same marker defined in subsection 9.3.4 of reference [7].

NOTE – A CSM together with a pseudo-randomized codeword is called an SMCW. When a 16-symbol CSM is used, the i^{th} SMCW is denoted

$$\mathbf{e}^i = e_0^i, e_1^i, \dots, e_{n+15}^i.$$

5.9 PN SPREADER

Each SMCW shall be PN-spread by a factor of q_u , $q_u \in \{1, 2, 3, 4, 8, 16, 32, 64\}$, by repeating each binary symbol so that it appears q_u times and digit-wise adding it to a binary PN-like sequence $\mathbf{p} = p_0, p_1, \dots, p_{q_u-1}$, as follows: for $i \in \{0, 1, \dots, A-1\}$, the i^{th} PN-spread SMCW is \mathbf{f}^i , and for $j \in \{0, 1, \dots, (n+16)q_u-1\}$, the j^{th} binary symbol is given by

$$f_j^i = e_{\lfloor j/q_u \rfloor}^i \oplus p_{j \bmod q_u},$$

where p_0, p_1, p_2, \dots are the binary digits of the PN sequence given in Table 5-1 and where $\lfloor x \rfloor$ is the integer part of x .

Table 5-1: PN Sequence

q_u	p
1	0
2	01
3	011
4	0110
8	0111 0010
16	0111 0011 0110 1000
32	0111 0101 0001 1110 1101 1100 1001 0000
64	0100 1110 1101 0011 1010 0001 0101 1101 0110 0101 1100 1100 1100 1111 0000 1000

5.10 2-PPM SLOT MAPPER

5.10.1 For $i \in \{0, 1, \dots, A-1\}$, the i^{th} PN spread SMCW shall be a sequence of binary symbols denoted

$$\mathbf{f}^i = f_0^i, f_1^i, \dots, f_{(n+16)q_u-1}^i.$$

5.10.2 To modulate with 2-PPM, each binary symbol f_j^i shall be mapped to a vector of length 2 by

$$\mathbf{g}_j^i = (g_{j,0}^i, g_{j,1}^i) = \begin{cases} (1,0), & \text{if } f_j^i = 0 \\ (0,1), & \text{otherwise} \end{cases};$$

the length $2(n+16)q_u$ sequence of slots for the i^{th} SMCW is denoted

$$\mathbf{g}^i = g_{0,0}^i, g_{0,1}^i, g_{1,0}^i, g_{1,1}^i, \dots, g_{(n+16)q_u-1,0}^i, g_{(n+16)q_u-1,1}^i.$$

5.11 GUARD SLOT INSERTION

After each 2-PPM symbol comprising two slots, a guard time of two slots shall be inserted. For $i \in \{0, 1, \dots, A-1\}$, the result for the i^{th} SMCW in the slot sequence is denoted

$$\mathbf{h}^i = g_{0,0}^i, g_{0,1}^i, 0, 0, g_{1,0}^i, g_{1,1}^i, 0, 0, \dots, g_{(n+16)q_u-1,0}^i, g_{(n+16)q_u-1,1}^i, 0, 0.$$

5.12 AOS/USLP TRANSFER FRAME VALIDATION AND SEQUENCE INDICATOR

5.12.1 At the receiving end AOS/USLP transfer frames shall be validated using the same procedure defined in 4.11.

5.12.2 At the receiving end a Sequence Indicator shall be computed using the same procedure defined in 4.12.

6 MANAGED PARAMETERS

6.1 OVERVIEW

Some parameters associated with synchronization and channel coding are handled by management rather than by an inline communications protocol. The managed parameters are those that tend to be static for long periods of time, and whose change generally signifies a major reconfiguration of the synchronization and channel coding systems associated with a particular mission. Through the use of a management system, management conveys the required information to the channel coding and synchronization systems.

In this section, the managed parameters used by channel coding and synchronization systems are listed. These parameters are defined in an abstract sense and are not intended to imply any particular implementation of a management system.

6.2 MANAGED PARAMETERS FOR TELEMETRY SIGNALING

6.2.1 The managed parameters for the HPE telemetry signaling shall be those specified in Table 6-1.

Table 6-1: Managed Parameters for HPE Telemetry Signaling

Managed Parameter	Allowed Values
TM/AOS/USLP transfer frame length (octets)	Integer (max 65536)
PPM order, M	4, 8, 16, 32, 64, 128, 256
Code rate, r	1/3, 1/2, 2/3
Number of rows in channel interleaver, N	BN shall be a multiple of S , which in turn shall be a multiple of N .
Shift register length increment in channel interleaver, B	
Repeat factor, q_d	1, 2, 3, 4, 8, 16, 32

6.2.2 The managed parameters for the O3K with RS coding telemetry signaling shall be those specified in Table 6-2.

Table 6-2: Managed Parameters for O3K RS Telemetry Signaling

Managed Parameter	Allowed Values
TM/AOS/USLP transfer frame length (octets)	Integer (max 65536)
Symbol Interleaving Depth (I)	1, 2, 3, 4, 5, 8
Repeat factor list	$\{q_{d,0}; q_{d,1}; \dots; q_{d,n}\}$, with $0 \leq n \leq 13$ and $q_{d,i} = 2^w$, with $0 \leq w \leq (13 - \log_2(T \times 10 \times 10^9))$, where T , the slot width in nanoseconds, is defined in [3].
Interleaver block parameter K	Multiple of 8 and factor of L
Nominal number of interleaver rows N_N	Integer in the range 1 to $2^{23} - 1$
Number of rows in the interleaver N	$N_N; N_N/q_d$
Transfer frame adaptation	Used, not used
IFS field (counter)	Used, not used
Number of blocks in a sync layer subframe	N_L

6.2.3 The managed parameters for the O3K LDPC telemetry signaling shall be those specified in Table 6-3.

NOTE – The synchronization and channel coding parameters for O3K with LDPC coding are handled both by management and by an inline communications protocol.

Table 6-3: Managed Parameters for O3K LDPC Telemetry Signaling

Managed Parameter	Allowed Values
TM/AOS/USLP transfer frame length (octets)	Integer (max 65536)
The number of blocks of length 30720 within a sync layer subframe, N_L	Integer, $N_L=1$ to N $\frac{SF \times N}{N_L} \in \mathbb{N}$
Number of rows of the emitter configuration table $N_{\text{ModeTable}}$	Integer, 1 to 62
Emitter configuration Mode table (as defined in 4.8.2.6.4)	<ul style="list-style-type: none"> – Mode ID – Description: text – Code rate, $r \in \{1/2, 9/10\}$ – Repeat factor $SF \in \{1, 2, 4, 8, 16\}$ – Number of rows in the interleaver N: integer from 1 to 2^{18} – Interleaver block parameter $K \in \{64, 128, 256, 512, 1024\}$
Transfer frame adaptation	Used, not used

6.3 MANAGED PARAMETERS FOR AOS/USLP TRANSFER FRAME SIGNALING

The managed parameters for a HPE AOS/USLP transfer frame signaling shall be those specified in Table 6-4.

Table 6-4: AOS/USLP Transfer Frame Signaling

Managed Parameter	Allowed Values
AOS/USLP transfer frame length (octets)	Integer (max 65536)
Input block length, k	64, 256, 1024
PN spreading factor, q_u	1, 2, 3, 4, 8, 16, 32, 64
Channel interleaver	Used, not used
Number of rows in channel interleaver, N_u	$B_u N_u$ shall be a multiple of 128, which in turn shall be a multiple of N_u .
Shift register length increment in channel interleaver, B_u	

ANNEX A

PROTOCOL IMPLEMENTATION CONFORMANCE STATEMENT PROFORMA

(NORMATIVE)

A1 INTRODUCTION

A1.1 OVERVIEW

This annex provides the Protocol Implementation Conformance Statement (PICS) Requirements List (RL) for an implementation of Optical Communications Coding and Synchronization (CCSDS 142.0-B-2). The PICS for an implementation is generated by completing the RL in accordance with the instructions below. An implementation claiming conformance must satisfy the mandatory requirements referenced in the RL.

The RL support column in this annex is blank. An implementation's completed RL is called the PICS. The PICS states which capabilities and options have been implemented. The following can use the PICS:

- the implementer, as a checklist to reduce the risk of failure to conform to the standard through oversight;
- a supplier or potential acquirer of the implementation, as a detailed indication of the capabilities of the implementation, stated relative to the common basis for understanding provided by the standard PICS proforma;
- a user or potential user of the implementation, as a basis for initially checking the possibility of interworking with another implementation (it should be noted that, while interworking can never be guaranteed, failure to interwork can often be predicted from incompatible PICSes);
- a tester, as the basis for selecting appropriate tests against which to assess the claim for conformance of the implementation.

A1.2 ABBREVIATIONS AND CONVENTIONS

The RL consists of information in tabular form. The status of features is indicated using the abbreviations and conventions described below.

Item Column

The item column contains sequential numbers for items in the table.

Description Column

The description column contains a brief description of the item. It implicitly means ‘Is this item supported by the implementation?’

Reference Column

The reference column indicates the relevant subsection of *Non-Coherent Optical Communications Coding and Synchronization*, CCSDS 142.0-B-2 (this document).

Status Column

The status column uses the following notations:

M	Mandatory.
O	Optional.
C:<status>	Indicates that the status applies for the given subordinate item when the parent item is supported and is not applicable otherwise.
1+	One or more of the allowed values must be supported.
N/A	Not applicable.

Support Column Symbols

The support column is to be used by the implementer to state whether a feature is supported by entering Y, N, or N/A, indicating:

Y	Yes, supported by the implementation.
N	No, not supported by the implementation.
N/A	Not applicable.

The support column should also be used, when appropriate, to enter values supported for a given capability.

A1.3 INSTRUCTIONS FOR COMPLETING THE RL

An implementer shows the extent of compliance to the Recommended Standard by completing the RL; that is, the state of compliance with all mandatory requirements and the options supported are shown. The resulting completed RL is called a PICS. The implementer shall complete the RL by entering appropriate responses in the support or values supported column, using the notation described in A1.2. If a conditional requirement is inapplicable, N/A should be used. If a mandatory requirement is not satisfied, exception information must be supplied by entering a reference X_i , where i is a unique identifier, to an accompanying rationale for the noncompliance.

A2 PICS PROFORMA FOR NON-COHERENT OPTICAL COMMUNICATIONS CODING AND SYNCHRONIZATION (CCSDS 142.0-B-2)

A2.1 GENERAL INFORMATION

A2.1.1 Identification of PICS

Date of Statement (DD/MM/YYYY)	
PICS serial number	
System conformance statement cross-reference	

A2.1.2 Identification of Implementation Under Test (IUT)

Implementation Name	
Implementation Version	
Special Configuration	
Other Information	

A2.1.3 Identification of Supplier

Supplier	
Contact Point for Queries	
Implementation Name(s) and Versions	
Other information necessary for full identification, for example, name(s) and version(s) for machines and/or operating systems, System Name(s)	

A2.1.4 Identification of Specification

CCSDS 142.0-P-1.1	
Have any exceptions been required?	Yes [] No []
NOTE – A YES answer means that the implementation does not conform to the Recommended Standard. Non-supported mandatory capabilities are to be identified in the PICS, with an explanation of why the implementation is nonconforming.	

CCSDS RECOMMENDED STANDARD FOR
NON-COHERENT OPTICAL COMMUNICATIONS CODING AND SYNCHRONIZATION

A2.1.5 Requirements List

HPE telemetry signaling:

Item	Description	Reference	Status	Values Allowed	Item Supported or Values Supported
HPE-1	CCSDS transfer frames	4.2	M	N/A	
HPE-2	ASM	4.3.3	M	N/A	
HPE-3	Slicer	4.3.4	M	N/A	
HPE-4	Pseudo-randomizer	3.5 4.7	M	N/A	
HPE-5	CRC attachment	3.6	M	N/A	
HPE-6	Termination	3.7	M	N/A	
HPE-7	SCPPM encoder	3.8	M	N/A	
HPE-8	Channel interleaver	3.9	M	N/A	
HPE-9	(N, B) parameters for channel interleaver	3.9.3	1+	(See Table 6-1)	
HPE-10	Codeword synchronization marker	3.10	M	N/A	
HPE-11	Repeat factor	3.11 4.6	1+	1, 2, 3, 4, 8, 16, 32	
HPE-12	Slot mapper	3.12	M	N/A	
HPE-13	Guard slot insertion	3.13	M	N/A	
HPE-14	Transfer frame validation	4.11	M	N/A	

CCSDS RECOMMENDED STANDARD FOR
NON-COHERENT OPTICAL COMMUNICATIONS CODING AND SYNCHRONIZATION

SCPPM encoder features:

Item	Description	Reference	Status	Values Allowed	Item Supported or Values Supported
HPE-15	Convolutional code polynomial	3.8.2.1	M	N/A	
HPE-16	Initialization	3.8.2.2	M	N/A	
HPE-17	Convolutional code rate	3.8.2.3	1+	1/3, 1/2, 2/3	
HPE-18	Puncturing	3.8.2.3	M	N/A	
HPE-19	Code interleaver	3.8.3	M	N/A	
HPE-20	Accumulator	3.8.4	M	N/A	
HPE-21	PPM order	3.8.5	1+	4, 8, 16, 32, 64, 128, 256	
HPE-22	PPM symbol mapper	3.8.5	M	N/A	

O3K RS telemetry signaling:

Item	Description	Reference	Status	Values Allowed	Item Supported or Values Supported
O3KR-1	CCSDS transfer frames	4.2	M	N/A	
O3KR-2	ASM	4.3.3	O	Present or absent	
O3KR-3	Slicer	4.3.4	O	Present or absent	
O3KR-4	Forward Error Correction RS	4.4.2	M	N/A	
O3KR-5	Channel interleaver	4.5	O	Present or absent	
O3KR-6	(K, N, L) parameters for channel interleaver	4.5.1.2	1+	(See Table 6-2)	
O3KR-7	Repeat factor	4.6	M	1	
O3KR-8	Pseudo-randomizer	4.7.1	M	N/A	

CCSDS RECOMMENDED STANDARD FOR
NON-COHERENT OPTICAL COMMUNICATIONS CODING AND SYNCHRONIZATION

O3KR-9	Sync layer framing	4.8	M	N/A	
O3KR-10	FSM	4.8.1	M	N/A	
O3KR-11	Sync layer framing parameter N_L	4.8.1	M	1 to N (See Table 6-2)	
O3KR-12	Repeat symbol (q_d)	4.9	1+	(See Table 6-2)	
O3KR-13	Transfer frame validation	4.11	M	N/A	

O3K LDPC Telemetry signaling:

Item	Description	Reference	Status	Values Allowed	Item Supported or Values Supported
O3KL-1	CCSDS transfer frames	4.2	M	N/A	
O3KL-2	ASM	4.3.3	O	N/A	
O3KL-3	Slicer	4.3.4	O	N/A	
O3KL-4	Forward Error Correction LDPC	4.4.3	M	N/A	
O3KL-5	Channel interleaver	4.5	M	N/A	
O3KL-6	(K, N, L) parameters for channel interleaver	4.5.1.2	1+	(See Table 6-3)	
O3KL-7	Repeat factor	4.6	1+	1, 2, 4, 8, 16	
O3KL-8	Pseudo-randomizer	4.7.2	M	N/A	
O3KL-9	Sync layer framing	4.8	M	N/A	
O3KL-10	FSM	4.8.2.3.2.2	M	N/A	
O3KL-11	IBS	4.8.2.3.2.34 .8.2.3.2.4	M	N/A	
O3KL-12	IFS	4.8.2.3.3	M	N/A	
O3KL-13	IDLE sync layer subframe	4.8.2.4	O	N/A	
O3KL-14	IIBS	4.8.2.4	O	N/A	

CCSDS RECOMMENDED STANDARD FOR
NON-COHERENT OPTICAL COMMUNICATIONS CODING AND SYNCHRONIZATION

O3KL-15	Emitter configuration mode table	4.8.2.6.4	M	(See Table 6-3)	
O3KL-16	Mode ID	4.8.2.6.4.2	M	N/A	
O3KL-17	Repeat symbol (q_d)	4.9.1	M	1	
O3KL-18	Transfer frame validation	4.11	M	N/A	

HPE beacon and optional AOS transfer frame signaling:

Item	Description	Reference	Status	Values Allowed	Item Supported or Values Supported
HPE-23.1	AOS/USLP transfer frame	5.2	C:M	N/A	
HPE-23.2	Attached synchronization marker	5.3	C:M	N/A	
HPE-23.3	Slicer	5.4	C:M	N/A	
HPE-23.4	LDPC Encoder input block length, k	5.5	C:1+	64, 256, 1024	
HPE-23.5	Pseudo-randomizer	5.6	C:M	N/A	
HPE-23.6	Channel interleaver	5.7	C:O	N/A	
HPE-23.6.1	(N , B) parameters for channel interleaver	5.7	C:C:1+	See Table 6-4	
HPE-23.7	Codeword sync marker	5.8	M	N/A	
HPE-23.8	PN spreading factor	5.9	1+	1, 2, 3, 4, 8, 16, 32, 64	
HPE-23.9	2-PPM slot mapper	5.10	M	N/A	
HPE-23.10	Guard slot insertion	5.11	M	N/A	

ANNEX B

SERVICE

(NORMATIVE)

B1 OVERVIEW

B1.1 INTRODUCTION

This annex provides service definition in the form of primitives, which present an abstract model of the logical exchange of data and control information between the service provider and the service user. The definitions of primitives are independent of specific implementation approaches.

The parameters of the primitives are specified in an abstract sense and specify the information to be made available to the user of the primitives. The way in which a specific implementation makes this information available is not constrained by this specification. In addition to the parameters specified in this annex, an implementation can provide other parameters to the service user (e.g., parameters for controlling the service, monitoring performance, facilitating diagnosis, and so on).

B1.2 OVERVIEW OF THE SERVICE

For telemetry signaling, this Recommended Standard provides unidirectional (one-way) transfer of a sequence of CCSDS transfer frames (for examples, see references [1] and [2]) at a constant data rate over a Physical Channel across a space link, using one of a number of specified channel coding and modulation methods.

For AOS transfer frame signaling, the Recommended Standard provides unidirectional transfer of a sequence of fixed-length AOS transfer frames over a Physical Channel across a space link using a specified channel coding and modulation method.

B2 TELEMETRY SERVICE PARAMETERS

B2.1 OPTICAL TELEMETRY FRAME

B2.1.1 The Optical Telemetry (OTM) frame parameter is the service data unit of this service and shall be a CCSDS transfer frame as defined in reference [1] or [2].

B2.1.2 The length of any transfer frame transferred on a Physical Channel shall be established by management.

B2.2 QUALITY INDICATOR

The Quality Indicator parameter shall be used to notify the user at the receiving end of the service that the received transfer frame was not able to be successfully decoded.

B2.3 SEQUENCE INDICATOR

The Sequence Indicator parameter shall be used to notify the user at the receiving end of the service that one or more transfer frames of the Physical Channel have been lost as the result of a loss of frame synchronization.

B3 TELEMETRY SERVICE PRIMITIVES

B3.1 GENERAL

B3.1.1 The service primitives associated with this service are

- a) OTM ChannelAccess.request; and
- b) OTM ChannelAccess.indication.

B3.1.2 The OTM ChannelAccess.request primitive shall be passed from the service user at the sending end to the service provider to request that a frame be transferred through the Physical Channel to the user at the receiving end.

B3.1.3 The OTM ChannelAccess.indication shall be passed from the service provider to the service user at the receiving end to deliver a frame.

B3.2 OTM CHANNELACCESS.REQUEST

B3.2.1 Function

The OTM ChannelAccess.request primitive is the service request primitive for this service.

B3.2.2 Semantics

The OTM ChannelAccess.request primitive shall provide a parameter as follows:

OTM ChannelAccess.request (OTM Frame)

B3.2.3 When Generated

The ChannelAccess.request primitive shall be passed to the service provider to request it to process and send the frame.

B3.2.4 Effect on Receipt

Receipt of the OTM ChannelAccess.request primitive shall cause the service provider to perform the functions described in section 3 and to transfer the resulting pulsed slot sequence.

B3.3 OTM CHANNELACCESS.INDICATION

B3.3.1 Function

The OTM ChannelAccess.indication primitive is the service indication primitive for this service.

B3.3.2 Semantics

The OTM ChannelAccess.indication primitive shall provide parameters as follows:

OTM ChannelAccess.indication (OTM Frame, Quality Indicator, Sequence Indicator)

B3.3.3 When Generated

The OTM ChannelAccess.indication primitive shall be passed from the service provider to the service user to deliver a frame.

B3.3.4 Effect on Receipt

The effect of receipt of the OTM ChannelAccess.indication primitive by the service user is undefined.

B4 AOS/USLP TRANSFER FRAME SERVICE PARAMETER

The AOS/USLP Data Unit (ADU) frame parameter is the service data unit of this service, as defined in reference [2].

B5 AOS/USLP TRANSFER FRAME SERVICE PRIMITIVES

B5.1 GENERAL

B5.1.1 The service primitives associated with this service are

- a) ADU ChannelAccess.request; and
- b) ADU ChannelAccess.indication.

B5.1.2 The ADU ChannelAccess.request primitive shall be passed from the service user at the sending end to the service provider to request that a frame be transferred through the Physical Channel to the user at the receiving end.

B5.1.3 The ADU ChannelAccess.indication shall be passed from the service provider to the service user at the receiving end to deliver a frame.

B5.2 ADU CHANNELACCESS.REQUEST

B5.2.1 Function

The ADU ChannelAccess.request primitive is the service request primitive for this service.

B5.2.2 Semantics

The ADU ChannelAccess.request primitive shall provide a parameter as follows:

ADU ChannelAccess.request (ADU Frame)

B5.2.3 When Generated

The ADU ChannelAccess.request primitive shall be passed to the service provider at the sending end to request it to process and send the ADU frame.

B5.2.4 Effect on Receipt

Receipt of the ADU ChannelAccess.request primitive shall cause the service provider to perform the functions described in section 5 and to transfer the resulting pulsed slot sequence.

B5.3 ADU CHANNELACCESS.INDICATION

B5.3.1 Function

The ADU ChannelAccess.indication primitive is the service indication primitive for this service.

B5.3.2 Semantics

The ADU ChannelAccess.indication primitive shall provide parameters as follows:

ADU ChannelAccess.indication (ADU Frame, Quality Indicator, Sequence Indicator)

B5.3.3 When Generated

The ADU ChannelAccess.indication primitive shall be passed from the service provider to the service user to deliver an ADU frame.

B5.3.4 Effect on Receipt

The effect of receipt of the ADU ChannelAccess.indication primitive by the service user is undefined.

ANNEX C

EXPONENT MATRIX TABLES FOR LDPC CODES

(NORMATIVE)

This annex provides the exponent matrix tables for each of the supported QC-LDPC codes.

Table C-5: Masking Matrix Table for LDPC Code Rate $r = 1/2$

10 106 24 97 36 124 50 0 72 42 76 47 78 76 84 42 104 32 108 28 110 114 141 0
11 41 25 38 37 45 51 79 73 87 77 25 79 48 85 11 105 13 109 26 111 108 141 0 142 0
12 4 26 104 38 89 52 94 74 20 78 99 80 40 86 75 106 67 110 98 112 26 142 0 143 0
13 99 27 103 39 82 53 0 61 36 75 123 79 99 87 53 107 2 111 113 113 86 143 0 144 0
14 37 28 38 40 36 54 58 62 76 76 18 80 47 88 103 108 126 112 39 114 118 144 0 145 0
15 101 21 66 29 88 55 78 61 5 63 0 77 115 89 107 109 71 113 100 115 19 145 0 146 0
16 104 22 84 30 15 56 109 62 13 64 108 78 101 90 48 110 68 114 77 116 102 146 0 147 0
17 78 23 8 31 92 57 61 63 9 65 90 79 92 91 112 111 22 115 74 117 48 147 0 148 0
18 96 24 74 32 1 58 105 64 11 66 78 80 28 92 73 112 118 116 22 118 92 148 0 149 0
19 38 25 58 33 87 59 18 61 87 65 96 67 45 93 42 113 123 117 31 119 120 149 0 150 0
20 108 26 120 34 94 60 2 62 6 66 66 68 123 94 46 114 39 118 81 120 17 150 0 151 0
1 109 27 38 35 71 41 47 63 73 67 29 69 19 95 22 101 34 115 36 119 0 151 0 152 0
2 113 28 26 36 115 42 9 64 81 68 7 70 93 96 98 102 22 116 51 120 61 152 0 153 0
3 64 29 70 37 66 43 83 65 67 69 30 71 27 97 109 101 10 103 119 117 27 153 0 154 0
4 107 30 45 38 98 44 99 66 18 70 81 72 88 98 78 102 50 104 46 118 4 154 0 155 0
5 73 31 88 39 50 45 40 67 52 71 40 73 97 99 50 103 24 105 7 119 42 155 0 156 0
6 80 32 50 40 30 46 113 68 72 72 2 74 6 100 5 104 59 106 23 120 44 156 0 157 0
7 78 21 9 33 28 47 5 69 68 73 119 75 95 81 14 101 19 105 9 107 81 157 0 158 0
8 29 22 62 34 55 48 34 70 43 74 120 76 35 82 41 102 60 106 80 108 1 158 0 159 0
9 58 23 13 35 38 49 39 71 108 75 111 77 9 83 41 103 65 107 68 109 68 159 0 160 0
1 27 9 118 10 89 31 15 50 25 59 49 60 5 66 60 81 83 91 80 92 99 113 73 160 0 161 0
2 116 10 2 11 15 32 20 41 124 51 98 60 43 67 30 82 106 92 124 93 66 114 35 161 0 162 0
3 82 11 68 12 72 33 30 41 44 42 118 52 104 68 127 83 1 93 78 94 107 115 122 162 0 163 0
4 95 12 26 13 83 34 8 42 107 43 73 53 108 69 81 84 30 94 25 95 107 116 111 163 0 164 0
5 114 13 4 14 50 35 85 43 40 44 126 54 65 70 70 85 90 95 112 96 100 117 70 164 0 165 0
6 64 14 106 15 49 36 58 44 119 45 110 55 75 71 38 86 54 96 121 97 62 118 56 165 0 166 0
7 75 15 21 16 127 37 67 45 67 46 113 56 36 72 73 87 117 97 52 98 58 119 6 166 0 167 0
8 67 16 28 17 6 38 73 46 106 47 91 57 48 73 91 88 107 98 59 99 81 120 88 167 0 168 0
9 27 17 84 18 46 39 21 47 117 48 117 58 113 74 20 89 18 99 59 100 9 101 107 168 0 169 0
10 80 18 49 19 77 40 41 48 34 49 51 59 68 75 54 81 104 90 127 100 36 102 57 169 0 170 0
11 63 19 93 20 77 21 1 49 50 50 38 60 20 76 41 81 84 82 74 91 10 103 95 170 0 171 0
1 61 12 103 20 59 22 97 41 47 50 2 51 78 77 123 82 81 83 20 92 116 104 85 171 0 172 0
1 31 2 49 13 120 23 13 42 125 51 103 52 34 78 92 83 28 84 72 93 84 105 59 172 0 173 0
2 94 3 103 14 26 24 46 43 4 52 127 53 25 79 65 84 107 85 121 94 112 106 72 173 0 174 0
3 42 4 62 15 110 25 5 44 49 53 67 54 74 80 70 85 2 86 127 95 6 107 28 174 0 175 0
4 60 5 120 16 119 26 85 45 54 54 94 55 105 61 64 86 77 87 42 96 76 108 80 175 0 176 0
5 84 6 92 17 52 27 55 46 35 55 92 56 50 62 117 87 110 88 127 97 19 109 10 176 0 177 0
6 28 7 72 18 54 28 60 47 5 56 117 57 117 63 72 88 49 89 45 98 107 110 118 177 0 178 0
7 21 8 105 19 11 29 64 48 8 57 103 58 26 64 29 89 115 90 19 99 55 111 73 178 0 179 0
8 118 9 102 20 0 30 56 49 66 58 9 59 109 65 16 90 13 91 94 100 12 112 5 179 0 180 0
5 117 12 57 121 0
6 96 13 30 122 0
7 29 14 81 123 0
8 71 15 111 124 0
9 31 16 44 125 0
10 17 17 2 126 0
11 61 18 95 127 0
12 9 19 79 128 0
13 111 20 57 129 0
1 20 14 110 130 0
2 41 15 82 131 0
3 28 16 117 132 0
4 74 17 33 133 0
5 23 18 30 134 0
6 2 19 35 135 0
7 119 20 123 136 0
1 25 8 99 137 0
2 109 9 89 138 0
3 14 10 27 139 0

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4 85 11 13 140 0
8 125 12 88 43 0 88 124 153 56 159 105 181 0
9 72 13 52 44 114 89 118 154 16 160 96 182 0
10 83 14 39 45 119 90 79 141 126 155 110 183 0
11 127 15 102 46 42 91 111 142 10 156 87 184 0
12 100 16 37 47 69 92 41 143 69 157 94 185 0
13 105 17 120 48 47 93 12 144 0 158 30 186 0
14 56 18 12 49 84 94 29 145 86 159 29 187 0
15 48 19 97 50 60 95 57 146 23 160 115 188 0
16 65 20 27 51 106 96 98 141 120 147 0 189 0
1 50 17 52 52 32 97 14 142 60 148 101 190 0
2 101 18 105 53 106 98 118 143 10 149 61 191 0
3 48 19 2 54 126 99 50 144 26 150 94 192 0
4 108 20 8 55 54 100 22 145 37 151 24 193 0
1 113 5 74 56 42 81 81 146 121 152 79 194 0
2 78 6 30 57 47 82 42 147 13 153 33 195 0
3 81 7 66 58 47 83 50 148 26 154 61 196 0
4 34 8 101 59 81 84 78 149 70 155 24 197 0
5 59 9 120 60 75 85 94 150 2 156 99 198 0
6 92 10 39 41 61 86 118 151 98 157 22 199 0
7 45 11 58 42 75 87 42 152 52 158 35 200 0
18 78 47 0 56 90 100 78 173 123 201 0
19 20 48 28 57 109 81 51 174 123 202 0
20 62 49 67 58 36 82 30 175 99 203 0
1 3 50 100 59 28 83 55 176 38 204 0
2 33 51 86 60 27 84 117 177 118 205 0
3 41 41 22 52 117 85 115 178 40 206 0
4 75 42 92 53 48 86 31 179 69 207 0
5 20 43 33 54 29 87 13 180 80 208 0
6 14 44 48 55 120 88 63 161 72 209 0
7 109 45 15 56 56 89 4 162 73 210 0
8 42 46 61 57 4 90 104 163 91 211 0
9 85 47 2 58 34 91 7 164 108 212 0
10 65 48 42 59 113 92 30 165 11 213 0
11 70 49 56 60 66 93 108 166 123 214 0
12 45 41 99 50 114 94 81 167 109 215 0
13 62 42 83 51 119 95 4 168 87 216 0
14 125 43 54 52 123 96 118 169 91 217 0
15 55 44 12 53 33 97 1 170 57 218 0
16 110 45 104 54 26 98 83 171 76 219 0
17 48 46 5 55 7 99 34 172 23 220 0
12 2 19 17 26 57 93 102 132 84 155 108 221 0
13 12 20 28 27 63 94 70 133 43 156 29 222 0
1 14 14 124 28 85 95 97 134 95 157 77 223 0
2 81 15 72 29 5 96 70 135 61 158 1 224 0
3 60 16 114 30 31 97 20 136 107 159 118 225 0
4 118 17 47 31 108 98 4 137 124 160 26 226 0
5 95 18 44 32 96 99 8 138 97 141 81 227 0
6 100 19 56 33 14 100 51 139 39 142 47 228 0
7 8 20 109 34 6 81 34 140 7 143 101 229 0
1 51 8 91 35 34 82 45 121 87 144 127 230 0
2 110 9 74 36 80 83 59 122 70 145 67 231 0
3 14 10 18 37 110 84 22 123 65 146 94 232 0
4 15 11 28 38 112 85 33 124 41 147 49 233 0
5 61 12 81 39 42 86 40 125 47 148 34 234 0
6 15 13 112 40 44 87 5 126 58 149 36 235 0
7 65 14 88 21 43 88 107 127 76 150 73 236 0
8 64 15 118 22 6 89 49 128 117 151 122 237 0
9 88 16 12 23 49 90 3 129 104 152 80 238 0
10 87 17 47 24 56 91 6 130 8 153 20 239 0
11 29 18 56 25 120 92 48 131 110 154 96 240 0
12 61 41 124 59 84 94 1 146 15 241 0
13 47 42 67 60 9 95 123 147 48 242 0
14 86 41 12 43 6 96 61 148 45 243 0
15 95 42 23 44 70 97 39 149 6 244 0
16 15 43 90 45 27 98 124 150 88 245 0
17 2 44 84 46 37 99 123 151 4 246 0
18 3 45 55 47 70 100 83 152 101 247 0
19 17 46 75 48 2 81 119 153 60 248 0
20 42 47 121 49 104 82 112 154 104 249 0
1 80 48 8 50 70 83 29 155 42 250 0
2 111 49 57 51 84 84 21 156 99 251 0
3 121 50 9 52 36 85 7 157 101 252 0
4 81 51 88 53 68 86 74 158 1 253 0
5 46 52 84 54 33 87 64 159 82 254 0
6 78 53 103 55 109 88 106 160 114 255 0

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7 118 54 20 56 48 89 78 141 111 256 0
8 68 55 23 57 3 90 79 142 6 257 0
9 97 56 5 58 99 91 9 143 27 258 0
10 79 57 110 59 68 92 83 144 75 259 0
11 109 58 36 60 102 93 105 145 108 260 0

Table C-6: Masking Matrix Table for LDPC Code Rate $r = 9/10$

6 50 13 101 15 109 16 30 29 102 38 49 39 33 40 64 49 46 61 71 62 63 63 95 82 39 87 42 89 56 96 100 107 32 109 96 111 50 113 9 131 124 135 97 139
101 144 119 152 103 157 37 158 78 159 107 173 84 181 88 184 78 188 119 195 65 205 36 209 113 212 105 229 0
7 8 14 58 16 78 17 106 30 116 39 71 40 39 41 57 50 30 62 125 63 74 64 27 83 69 85 37 88 0 90 74 108 120 110 81 112 99 114 78 132 31 133 63 136 5 140
105 153 43 158 26 159 33 160 112 174 64 182 83 185 43 189 108 196 52 206 69 210 96 213 122 229 0 230 0
8 53 15 86 17 18 18 31 31 93 40 49 41 61 42 48 51 68 63 28 64 7 65 105 84 23 86 12 89 24 91 119 97 81 111 51 113 23 115 3 121 65 134 13 137 16 141
101 154 109 159 25 160 33 161 110 175 42 183 107 186 49 190 17 197 15 207 69 211 121 214 62 230 0 231 0
9 123 16 99 18 45 19 125 32 110 41 6 42 47 43 32 52 98 64 98 65 57 66 46 73 22 87 18 90 41 92 77 98 115 112 126 114 87 116 73 122 23 135 121 138
104 142 53 155 120 160 52 161 45 162 0 176 68 184 113 187 43 191 25 198 101 208 5 212 2 215 46 231 0 232 0
10 26 17 103 19 127 20 25 33 40 42 65 43 42 44 15 53 84 65 34 66 102 67 116 74 34 88 44 91 22 93 81 99 119 113 11 115 72 117 126 123 43 136 47 139
56 143 110 156 2 161 16 162 15 163 61 177 85 185 110 188 34 192 89 199 102 209 105 213 35 216 125 232 0 233 0
11 1 18 7 20 11 21 53 34 114 43 48 44 69 45 118 54 39 66 35 67 84 68 97 75 93 89 126 92 86 94 104 100 16 114 77 116 7 118 52 124 25 137 19 140 24
144 51 145 124 162 26 163 31 164 53 178 13 181 58 186 115 189 54 200 113 205 82 210 52 214 97 233 0 234 0
12 51 19 88 21 119 22 82 35 84 44 114 45 100 46 37 55 52 67 58 68 89 69 20 76 90 90 74 93 85 95 110 101 123 115 84 117 107 119 40 125 102 133 0 138
92 141 93 146 65 163 46 164 46 165 110 179 50 182 28 187 12 190 110 201 29 206 55 211 89 215 21 234 0 235 0
1 117 20 33 22 111 23 111 36 109 45 28 46 25 47 118 56 123 68 120 69 101 70 61 77 95 91 5 94 88 96 36 102 63 116 31 118 2 120 66 126 55 134 67 139
106 142 118 147 65 164 94 165 73 166 124 180 113 183 85 188 108 191 83 202 76 207 97 212 27 216 40 235 0 236 0
2 39 21 30 23 109 24 65 25 14 46 15 47 79 48 122 57 30 69 57 70 88 71 33 78 74 85 33 92 34 95 41 103 118 109 98 117 49 119 108 127 3 135 15 140 79
143 15 148 59 165 97 166 107 167 29 169 26 184 98 189 116 192 30 203 49 205 112 208 54 213 123 236 0 237 0
3 123 13 28 22 120 24 27 26 22 37 21 47 115 48 80 58 49 70 5 71 85 72 82 79 88 86 19 93 117 96 112 104 103 110 118 118 109 120 81 128 13 136 93 141
94 144 100 149 85 166 76 167 114 168 95 170 123 181 9 185 9 190 51 204 31 206 80 209 86 214 95 237 0 238 0
4 106 13 14 14 36 23 14 27 18 37 23 38 89 48 103 59 19 61 43 71 65 72 2 80 73 85 47 87 7 94 23 105 6 109 63 111 103 119 67 129 25 133 26 137 48 142
109 150 70 157 100 167 30 168 2 171 35 182 68 186 97 191 125 193 42 207 42 210 86 215 69 238 0 239 0
5 20 14 93 15 53 24 86 28 11 37 10 38 55 39 116 60 88 61 11 62 124 72 1 81 101 86 91 88 96 95 108 106 71 110 103 112 41 120 33 130 0 134 75 138 114
143 84 151 15 157 40 158 0 168 7 172 93 183 25 187 29 192 76 194 35 208 108 211 102 216 4 239 0 240 0
7 80 11 7 12 127 17 11 26 26 28 72 35 54 42 104 53 114 56 55 57 16 68 17 73 54 77 17 80 0 95 19 99 79 103 101 106 123 119 74 121 85 124 119 129 125
144 23 151 4 154 127 155 62 161 13 170 57 173 73 176 26 186 18 196 28 201 102 203 29 206 110 240 0 241 0
1 42 8 22 12 94 18 111 27 101 29 63 36 43 43 69 54 96 57 38 58 125 69 115 74 62 78 26 81 51 96 87 100 115 104 57 107 110 120 7 122 4 125 77 130 12
133 127 152 56 155 110 156 55 162 106 171 91 174 11 177 17 187 114 197 125 202 7 204 88 207 119 241 0 242 0
1 50 2 36 9 58 19 49 25 40 28 18 30 53 44 115 55 120 58 46 59 47 70 72 75 46 79 70 82 2 85 76 101 60 105 103 108 10 109 26 123 101 126 83 131 20 134
112 145 50 153 99 156 123 163 26 172 24 175 104 178 76 188 118 193 117 198 126 203 45 208 11 242 0 243 0
2 91 3 30 10 38 20 60 26 126 29 119 31 127 45 76 56 66 59 98 60 68 71 22 76 105 80 54 83 51 86 99 97 77 102 44 106 50 110 37 124 85 127 35 132 42
135 17 145 83 146 44 154 77 164 38 173 90 176 56 179 5 189 18 194 19 199 10 204 51 209 17 243 0 244 0
3 111 4 1 11 101 21 43 27 94 30 19 32 68 46 121 49 37 57 74 60 112 72 0 77 48 81 39 84 55 87 39 98 14 103 90 107 115 111 69 121 81 125 125 128 86
136 108 146 4 147 11 155 83 165 97 174 18 177 19 180 73 190 18 193 39 195 100 200 63 210 63 244 0 245 0
4 114 5 68 12 79 22 94 28 56 31 44 33 53 47 36 49 83 50 93 58 60 61 13 73 3 78 45 82 20 88 43 99 89 104 12 108 125 112 105 122 63 126 4 129 58 137
61 147 87 148 52 156 35 166 88 169 76 175 30 178 119 191 56 194 36 196 22 201 39 211 34 245 0 246 0
1 13 5 45 6 77 23 93 29 49 32 112 34 10 48 89 50 37 51 106 59 69 62 22 74 32 79 102 83 72 89 102 97 78 100 106 105 64 113 32 123 57 127 18 130 85
138 20 145 46 148 107 149 76 167 50 170 5 176 64 179 29 192 10 195 22 197 44 202 19 212 103 246 0 247 0
2 72 6 8 7 114 24 89 30 18 33 9 35 67 37 63 51 92 52 92 60 119 63 78 75 13 80 6 84 44 90 15 98 29 101 36 106 61 114 24 124 111 128 41 131 87 139 41
146 125 149 14 150 23 168 32 171 86 177 14 180 5 181 89 196 63 198 84 203 87 213 113 247 0 248 0
3 93 7 79 8 75 13 78 31 19 34 29 36 15 38 15 49 96 52 32 53 90 64 63 73 121 76 33 81 44 91 66 99 49 102 1 107 11 115 13 125 39 129 27 132 12 140 24
147 7 150 34 151 12 157 59 169 82 172 126 178 65 182 18 197 119 199 12 204 127 214 67 248 0 249 0
4 28 8 8 9 125 14 7 25 122 32 109 35 5 39 39 50 113 53 58 54 102 65 23 74 37 77 42 82 13 92 122 100 43 103 113 108 117 116 19 121 72 126 71 130 35
141 92 148 59 151 26 152 13 158 123 170 55 173 117 179 106 183 34 193 47 198 46 200 6 215 107 249 0 250 0
5 101 9 49 10 33 15 89 26 52 33 33 36 93 40 27 51 0 54 20 55 27 66 93 75 116 78 77 83 58 93 18 97 4 101 59 104 72 117 75 122 20 127 56 131 40 142 40
149 18 152 75 153 123 159 109 171 62 174 57 180 21 184 60 194 31 199 107 201 21 216 59 250 0 251 0
6 37 10 85 11 87 16 70 25 74 27 1 34 6 41 91 52 37 55 45 56 124 67 91 76 6 79 1 84 13 94 44 98 113 102 42 105 113 118 87 123 11 128 62 132 84 143 32
150 110 153 117 154 126 160 70 169 119 172 105 175 121 185 55 195 101 200 65 202 113 205 82 251 0 252 0
2 78 12 113 217 0
1 110 3 81 218 0
2 36 4 100 219 0
3 13 5 4 220 0
4 25 6 56 221 0
5 109 7 38 222 0
6 44 8 13 223 0
7 92 9 57 224 0
8 46 10 2 225 0
9 117 11 78 226 0
10 108 12 96 227 0
1 85 11 26 228 0

ANNEX D

GENERATION OF GOLD CODES

(NORMATIVE)

D1 GOLD CODES DEFINITION

D1.1 A Gold code generator shall be used as shown in figure Figure D-1. It corresponds to the Gold code generator used for the real part of non-coherent return PN standardized in subsection 5.3 of reference [G3].

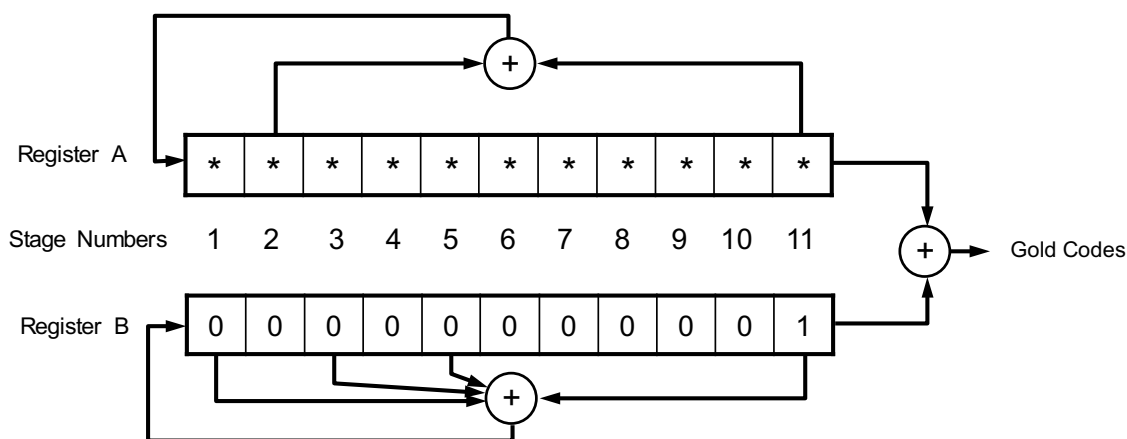


Figure D-1: Gold Sequence LFSRs in CCSDS 415.1-B-1 Publication (Real Part)

D1.2 This circuit shall comprise 2 single-shift registers of length 11, with associated code generator polynomials:

$$g_A(D) = D^{11} + D^2 + 1$$

$$g_B(D) = D^{11} + D^5 + D^3 + D + 1$$

D1.3 A zero shall be appended to the length-2047 Gold sequence to create a sequence of length 2048.

D1.4 The initial condition of register B is fixed; its value shall be [00000000001].

D1.5 The initial condition of registers A shall be defined to produce 65 preferred sequences. The 65 initial conditions correspond to the first 65 even decimals 2, 4, 6, ... , to 130, that converted in bits are: [00000000010], [00000000100], [00000000110], ... to [00010000010].

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NOTES

- 1 The first sequence, with initial condition of registers A = [00000000010] is reserved for the FSM defined in 4.8.2.
- 2 The second and third sequences, with initial condition of registers A respectively equal to [00000000100] and [00000000110], are reserved for Physical Layer framing as defined in 4.8.2.6.5.
- 3 The sequences from number 8 to 130 by step of 2 are the 62 sequences that are used for frame signaling as defined in 4.8.2.6.5.
- 4 The first 40 binary digits of the first Gold sequence (register A init = 2) are: 1100 0000 0001 0111 0011 1101 0010 0010 0101 0101. The first 40 binary digits of the second Gold sequence (register A init = 4) are: 1010 0000 0001 1000 1100 0011 0100 0100 0000 1111.
- 5 The provided generator polynomials are shift register polynomials. As explained in reference [G2], various conventions are used to map the polynomial terms to register stages in the shift register implementation. The convention used in reference [G2] and in this annex has the output at the cell with the highest number. If the cell numbering were reversed, the shift register and the output pattern would be the same, but the polynomial representation would be different: $g_{A'}(D) = D^{11} + D^9 + 1$ and $g_{B'}(D) = D^{11} + D^{10} + D^8 + D^6 + 1$. If the numbering convention is misinterpreted, a reverse pattern will be generated.

ANNEX E

SECURITY, SANA, AND PATENT CONSIDERATIONS

(INFORMATIVE)

E1 SECURITY CONSIDERATIONS

E1.1 SECURITY BACKGROUND

It is assumed that security is provided by encryption, authentication methods, and access control to be performed at a layer above the Physical Layer and Coding and Synchronization Sublayer. Mission and service providers are expected to select from recommended security methods one suitable to the specific application profile. Specification of these security methods and other security provisions is outside the scope of this Recommended Standard.

The coding layer has the objective of delivering data with the minimum possible number of residual errors. An LDPC, RS, or other code with CRC code needs to be used to ensure that residual errors are detected and the frame flagged. There is an extremely low probability of additional undetected errors that may escape this scrutiny. These errors may affect the encryption process in unpredictable ways, possibly affecting the decryption stage and producing data loss, but will not compromise the security of the data.

E1.2 SECURITY CONCERNS

Security concerns in the areas of data privacy, authentication, access control, availability of resources, and auditing are to be addressed in higher layers and are not related to this Recommended Standard.

E1.3 CONSEQUENCES OF NOT APPLYING SECURITY

There are no specific security measures prescribed for the coding layer. Therefore, consequences of not applying security are only imputable to the lack of proper security measures in other layers. Residual undetected errors may produce additional data loss when the link carries encrypted data.

E2 SANA CONSIDERATIONS

The recommendations of this document do not require any action from SANA.

E3 PATENT CONSIDERATIONS

The CCSDS draws attention to the fact that it is claimed that compliance with this document may involve the use of patents concerning Low-Density Parity-Check Coding. The CCSDS takes no position concerning the evidence, validity, and scope of these patent rights. The holders of these patent rights have assured the CCSDS that they are willing to negotiate licenses under reasonable and non-discriminatory terms and conditions with applicants throughout the world. In this respect, the statements of the holders of these patent rights are registered with CCSDS. Information can be obtained from the CCSDS Secretariat at the address indicated on page i. Contact information for the holders of these patent rights is provided below.

Potential user agencies should direct their requests for licenses to:

Office of Technology Transfer
California Institute of Technology
1200 E. California Blvd., Mail Code 210-85
Pasadena, CA 91125
Tel: +1 626 395 3822, +1 626 577 2528
E-mail: Vieregg@Caltech.edu

The LDPC codes used in this document were developed by Airbus Defence and Space. For the LDPC 9/10-rate code, one patent was identified:

- US Patent 7,343,539, “ARA Type Protograph Codes”, issued March 11th, 2008.

This patent was already used for the AR4JA LDPC in [7]. Caltech provided to the CCSDS a letter from the Office of Technology Transfer at Caltech that grants free use of these patents for scientific space applications. Considering that the LDPC 9/10-rate code of O3K and the LDPC of [7] cover the same usage, the same patent considerations are applied.

There are dozens, probably hundreds, of patents filed on the general topics of LDPC codes and LDPC decoders. In this environment, it is difficult to know what is covered and what is not. However, many companies have successfully implemented commercial products including LDPC encoders and decoders.

Attention is drawn to the possibility that some of the elements of this document may be the subject of patent rights other than those related to inventions of CCSDS member agencies. Implementers are cautioned that there are many patents filed on the general topics of channel coding and synchronization, including those related to LDPC codes and LDPC decoders.

ANNEX F

ABBREVIATIONS AND TERMS

(INFORMATIVE)

F1 OVERVIEW

This annex lists key acronyms and terms that are used throughout this Recommended Standard to describe synchronization and channel coding.

F2 ABBREVIATIONS

2-PPM	binary pulse position modulation
ADU	AOS/USLP data unit
AOS	Advanced Orbiting Systems
ARA	accumulate-repeat-accumulate
ASM	attached synchronization marker
CRC	cyclic redundancy check
CSM	codeword synchronization marker
FECF	frame error control field
FSM	frame synchronization marker
HPE	high photon efficiency
IBS	in-band signaling
IIBS	IDLE in-band signaling
IFS	interleaver frame signaling
ISL	IDLE sync layer
ISLF	IDLE sync layer subframe
ISLFM	IDLE sync layer subframe marker
IUT	implementation under test

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LDPC	low-density parity-check
LSB	least significant bit
MSB	most significant bit
NRZ	non-return-to-zero
O3K	optical on-off keying
OOK	on-off keying
OSI	Open System Interconnection
OTM	optical telemetry
PCM	parity-check matrix
PBRL	protograph-based raptor-like
PICS	protocol implementation conformance statement
PLOP	Physical Layer Operations Procedure
PN	pseudo-random noise
PPM	pulse position modulation
PRBS	pseudo-random binary sequence
QC	quasi-cyclic
RF	radio frequency
RS	Reed-Solomon
RSM	range synchronization marker
SCPPM	serially concatenated convolutionally coded pulse position modulation
<i>SF</i>	repeat factor/spreading factor
SLFM	sync layer frame marker
SLFRAME	sync layer frame
SMCW	synchronization-marked codeword
SMTF	synchronization-marked transfer frame

SSLFM	secondary sync layer frame marker
TM	telemetry
USLP	Unified Space Data Link Protocol

F3 TERMS

channel symbol: The unit of output of the innermost encoder.

code rate: The average ratio of the number of binary digits at the input of an encoder to the number of binary digits at its output.

codeword: Of an (n,k) block code, a sequence of n channel symbols that are produced by encoding a sequence of k information symbols.

Coding and Synchronization Sublayer: That sublayer of the Data Link Layer used by CCSDS space link protocols, which uses a prescribed coding technique to reliably transfer transfer frames through the potentially noisy Physical Layer.

constraint length: In convolutional coding, the number of consecutive input symbols needed to determine the value of the output symbols at any time.

convolutional code: A code in which a number of output symbols are produced for each input symbol. Each output symbol is a linear combination of the current input bit and some or all of the previous $k-1$ input bits, where k is the constraint length of the code.

slot measurement: Receiver estimate of the intensity of light, number of photons observed, or related statistic in a slot of the received transmission.

synchronization-marked transfer frame, SMTF: The data unit that consists of the ASM and the transfer frame.

ANNEX G

INFORMATIVE REFERENCES

(INFORMATIVE)

- [G1] B. Moision and J. Hamkins. “Coded Modulation for the Deep-Space Optical Channel: Serially Concatenated Pulse-Position Modulation.” *IPN Progress Report* 42-161 (May 15, 2005).
- [G2] *Data Transmission and PN Ranging for 2 GHz CDMA Link via Data Relay Satellite*. Issue 1. Report Concerning Space Data System Standards (Green Book), CCSDS 415.0-G-1. Washington, D.C.: CCSDS, April 2013.
- [G3] *Generic Framing Procedure*. ITU-T Recommendation G.7041/Y.1303. Geneva: ITU, 2016.
- [G4] *Data Transmission and PN Ranging for 2 GHz CDMA Link via Data Relay Satellite*. Issue 1. Recommendation for Space Data System Standards (Blue Book), CCSDS 415.1-B-1. Washington, D.C.: CCSDS, September 2011.

NOTE – Normative references are listed in 1.9.